

For DB build
SOUTH BRIDGE
HM75 QPEG Q0
BD82PPSM
P/N : 6019B0919101

DB BUILD QUANTITY LIST :

1310A2493104	10 PCS	CR-SG + H-VRAM + GIGA LAN
1310A2493105	72 PCS	CR-SG + S-VRAM + 10/100 LAN
1310A2493106	55 PCS	CR-UMA + GIGA LAN
1310A2493201	137 PCS	POWER /B
1310A2493601	137 PCS	PICK BTN /B
1310A2493701	137 PCS	USB /B

MOTHER BOARD PCB
P/N : 6050A2493101

POWER BOARD PCB
P/N : 6050A2493201

USB BOARD PCB
P/N : 6050A2493601

TOUCHPAD BOARD PCB
P/N : 6050A2493701

HARVEY 14

CR / HR UMA / DIS

2011.09.27

HARVEY 14 ID LIST
HURON RIVER UMA : 0X1854
HURON RIVER DIS : 0X1855
CHIEF RIVER UMA : 0X1856
CHIEF RIVER DIS : 0X1857

SUB SYSTEM ID :
HP : 0X103C

BASE SCHEMATIC :
CR-SG + SAMSUNG VRAM + 10/100 LAN + 90W ADAPTER + SUPPORT 27MHZ GREEN CLK + USB2.0 CONN

PAD402 不開鋼板 RTC Rest
PAD4500 不開鋼板 SM_Vrer
PAD2 開
PAD4 開
PAD400 開
PAD4700 開
PAD500 開
PAD508 開
PAD6015 開
PAD6100 開
PAD6103 開
PAD6105 開
PAD6110 開
PAD6150 開
PAD6200 開
PAD6210 開
PAD6220 開
PAD6300 開
PAD6301 開
PAD6310 開
PAD6510 開
PAD6500 開
PAD6610 開
PAD6710 開
PAD6750 開
PAD6970 開
PAD9000 開
PAD9001 開
PAD9200 開

GPU VRAM TYPE:

U5 , U6 , U7 , U8

SAMSUNG 1GB C-DIE
PN : 6019B0818601

R2 : NA
R3 : NA
R33 : NA
R61 : NA

HYNIX 1GB D-DIE
PN : 6019B0938301

R2 : MOUNT OR : 60130B0000ZT
R3 : NA
R33 : NA
R61 : NA

SAMSUNG 512KB

R2 : NA
R3 : MOUNT
R33 : NA
R61 : NA

HYNIX 512MB

R2 : MOUNT
R3 : MOUNT
R33 : NA
R61 : NA

DIS GREEN CLK SUPPORT 27MHZ

U9 SLG3NB300V P/N : 6019B0941101
C188 MOUNT 0.1UF : 6010A0036403
R126 MOUNT 10R : 60130B10000X
D4400 NA P/N : 6011A0026803

UMA GREEN CLK

U9 SLG3NB250V 6019B0934701
C188 NA
R126 NA
D4400 NA

DIS ADAPTER 90W

R802 MOUNT 100K : 60130B1040ZT
R769 NA

UMA ADAPTER 65W

R802 NA
R769 MOUNT 100K : 60130B1040ZT

USB 3.0 CONN

CN518 6012B0370301
C2405 MOUNT 0.1UF : 6010A0036403
C2406 MOUNT 0.1UF : 6010A0036403
D2400 NA >> ESD
L470 MOUNT P/N : 6014B0177901
L471 MOUNT P/N : 6014B0177901

USB 2.0 CONN

CN518 6012B0370102
C2405 NA
C2406 NA
D2400 NA >> ESD
L470 NA
L471 NA

BOARD ID	CR-UMA HR-UMA INTEL	CR-SG HR-SG SEYMOUR	CR-SG HR-SG THAMES
ID0-HI R960	0	1	0
ID1-HI R905	0	0	1
ID2-HI R902	0	0	0
ID3-HI R967	0	0	0
ID4-HI R899	0	0	0
ID5-HI R914	1	1	1
ID0-LO R961	1	0	1
ID1-LO R903	1	1	0
ID2-LO R904	1	1	1
ID3-LO R968	1	1	1
ID4-LO R900	1	1	1
ID5-LO R920	0	0	0

ID0 = GPIO40 RXXX = 10K : 60130B1030ZT
ID1 = GPIO41 0 : RXXX NA 1 : RXXX MOUNT
ID2 = GPIO42
ID3 = GPIO43
ID4 = GPIO9
ID5 = GPIO10

BOARD_ID5 ONLY FOR WEBCAN USE
FOR HD WEB CAM BOARD_ID5 PULL P3V3A
FOR VGA WEB CAM BOARD_ID5 PULL GND

M/B ID	DB	SI	PV	MV
R844	MOUNT	NA	MOUNT	NA
R824	MOUNT	MOUNT	NA	NA
R845	NA	MOUNT	NA	MOUNT
R828	NA	NA	MOUNT	MOUNT

RXXX = 10K : 60130B1030ZT NOV JAN MAR

RTL8161FH = 6019B0928101(10/100/1000)
RTL8165EH = 6019B0928301(10/100)

RTL8165EH(10/100) LDO MODE

R408 : MOUNT ----> CHOOSE LDO MODE OR : 60130B0000ZT
R407 : NA ----> CHOOSE SWITCHING MODE
R413 : NA
C404 : NA
C405 : NA
C406 : NA
C409 : NA
C411 : NA
C415 : NA
C417 : NA
C420 : NA
L400 : NA
U470 : NA

U502 : MOUNT 6016B0008101
U400 : MOUNT 6019B0928301 (10/100 LAN)

RTL8161FH(GIGA-LAN) SWITCHING MODE

R408 : NA ----> CHOOSE LDO MODE
R407 : MOUNT ----> CHOOSE SWITCHING MODE OR : 60130B1030ZT
R413 : MOUNT OR_0603 : 60130B000000Z
C404 : MOUNT 4.7UF : 6010B0009904
C405 : MOUNT 0.1UF : 6010A0036403
C406 : MOUNT 0.1UF : 6010A0036403
C409 : MOUNT 0.1UF : 6010A0036403
C411 : MOUNT 0.1UF : 6010A0036403
C415 : MOUNT 0.1UF : 6010A0036403
C417 : MOUNT 4.7UF : 6010B0009904
C420 : MOUNT 0.1UF : 6010A0036403
L400 : MOUNT P/N : 6014B0200401
U470 : MOUNT 6016B0010401
U502 : NA
U400 : MOUNT 6019B0928101 (GIGALAN)

INVENTEC - 2002.2010		
DATE	CHANGE NO.	REV

DRAWER	EE	DATE	POWER	DATE	INVENTEC			
DESIGN					MODEL,PROJECT,FUNCTION			
CHECK					HARVEY 14			
					SIZE	CODE	DOCNUMBER	REV
					CS	1310xxxx-9-0		NOI
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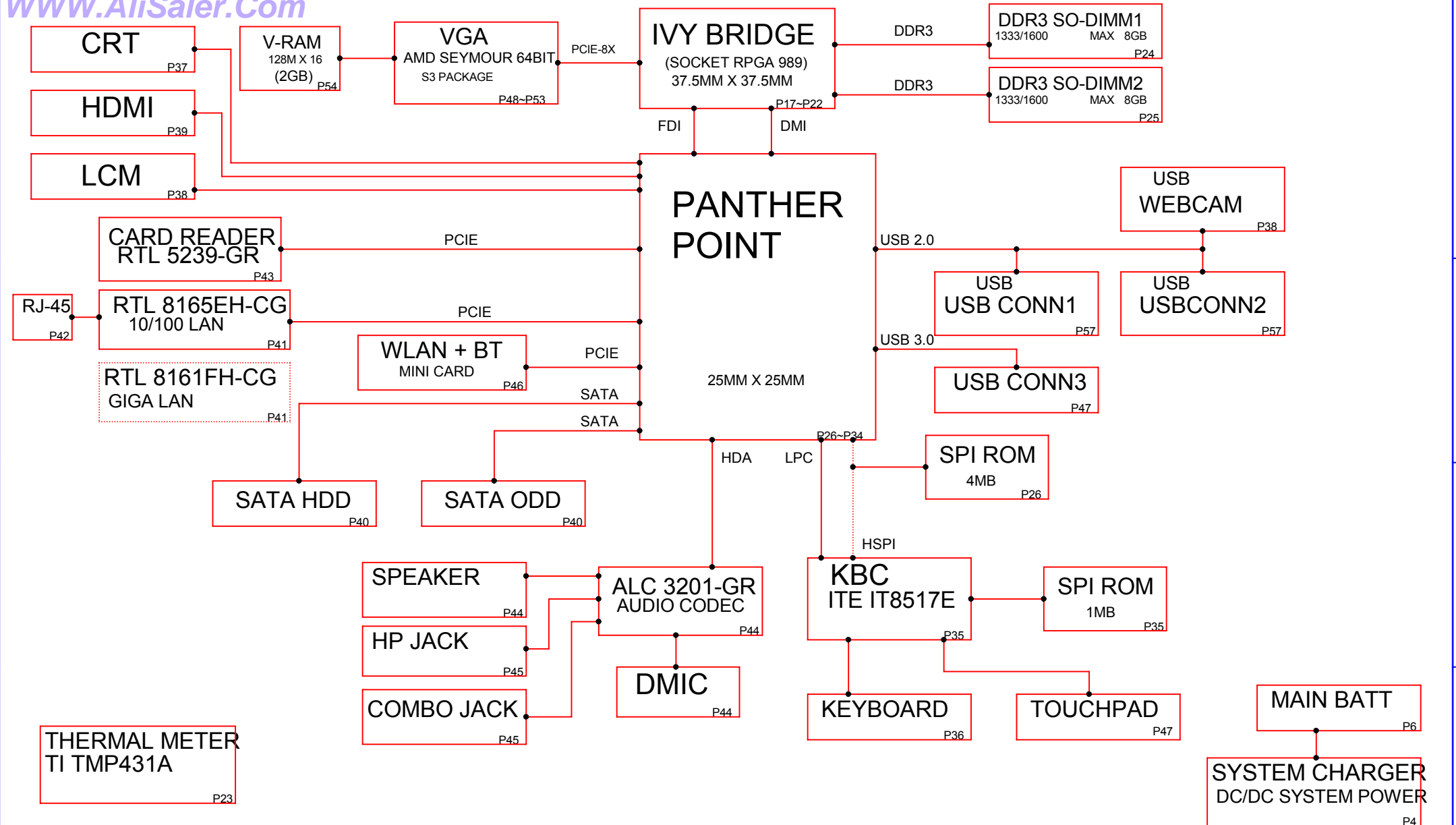
01	Project Name	26	PCH-1	51	SEYMOUR POWER
02	Page Index	27	PCH-2	52	SEYMOUR DP-POWER & LVDS
03	Block Diagram	28	PCH-3	53	SEYMOUR MEMORY INTERFACE
04	Power Procedure	29	PCH-4	54	VRAM DDR3
05	Charger	30	PCH-5	55	DGPU POWER EE
06	Battery Connector	31	PCH-6	56	DGPU POWER
07	P3V3A, P5V0A	32	PCH-7	57	USB, POWERBUTTON ,TP DB
08	P1V5	33	PCH-8		
09	P1V05S	34	PCH-9		
10	P1V8S	35	EC ITE8517E		
11	PVSA	36	KB & LED		
12	PVCORE-1	37	CRT		
13	PVCORE-2	38	LCD & WEBCAN		
14	PORT & EMI PART	39	HDMI		
15	P3V3S, P5V0S	40	SATA HDD& ODD		
16	GREEN CLK	41	LAN		
17	CPU -1	42	RJ-45 CONN		
18	CPU-2	43	CARD READER		
19	CPU-3	44	AUDIO CODEC		
20	CPU-4	45	HP & MIC JACK		
21	CPU-5	46	WLAN		
22	CPU-6	47	USB 3.0 CONN		
23	Thermal & Fan	48	SEYMOUR PCI-E INTERFACE		
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25	DDR3-2	50	SEYMOUR THERMAL SENSOR		

INVENTEC

TITLE			
MODEL,PROJECT,FUNCTION			
INDEX			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxxx-0-0	X01

CHANGE by XXX DATE 21-OCT-2002

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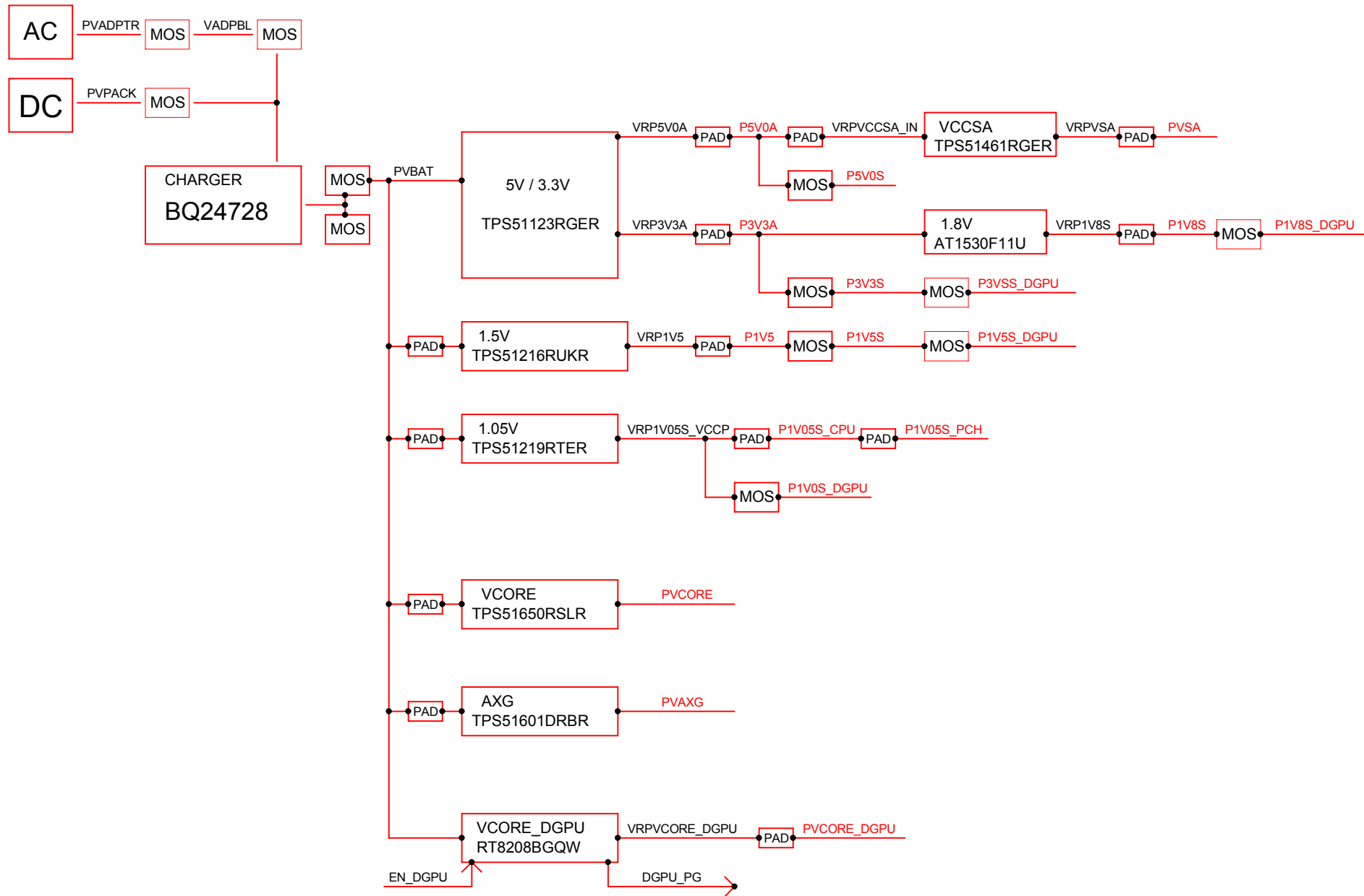


INVENTEC

TITLE			
MODEL, PROJECT, FUNCTION			
Block Diagram			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxx-0-0	X01

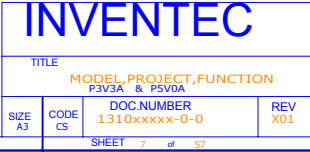
CHANGE by: XXX DATE: 21-OCT-2002

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INVENTEC			
TITLE MODEL PROJECT FUNCTION POWER PROCEDURE			
SIZE A3	CODE CS	DOC NUMBER 1310xxxxx-0-0	REV X01
SHEET 4 of 57			





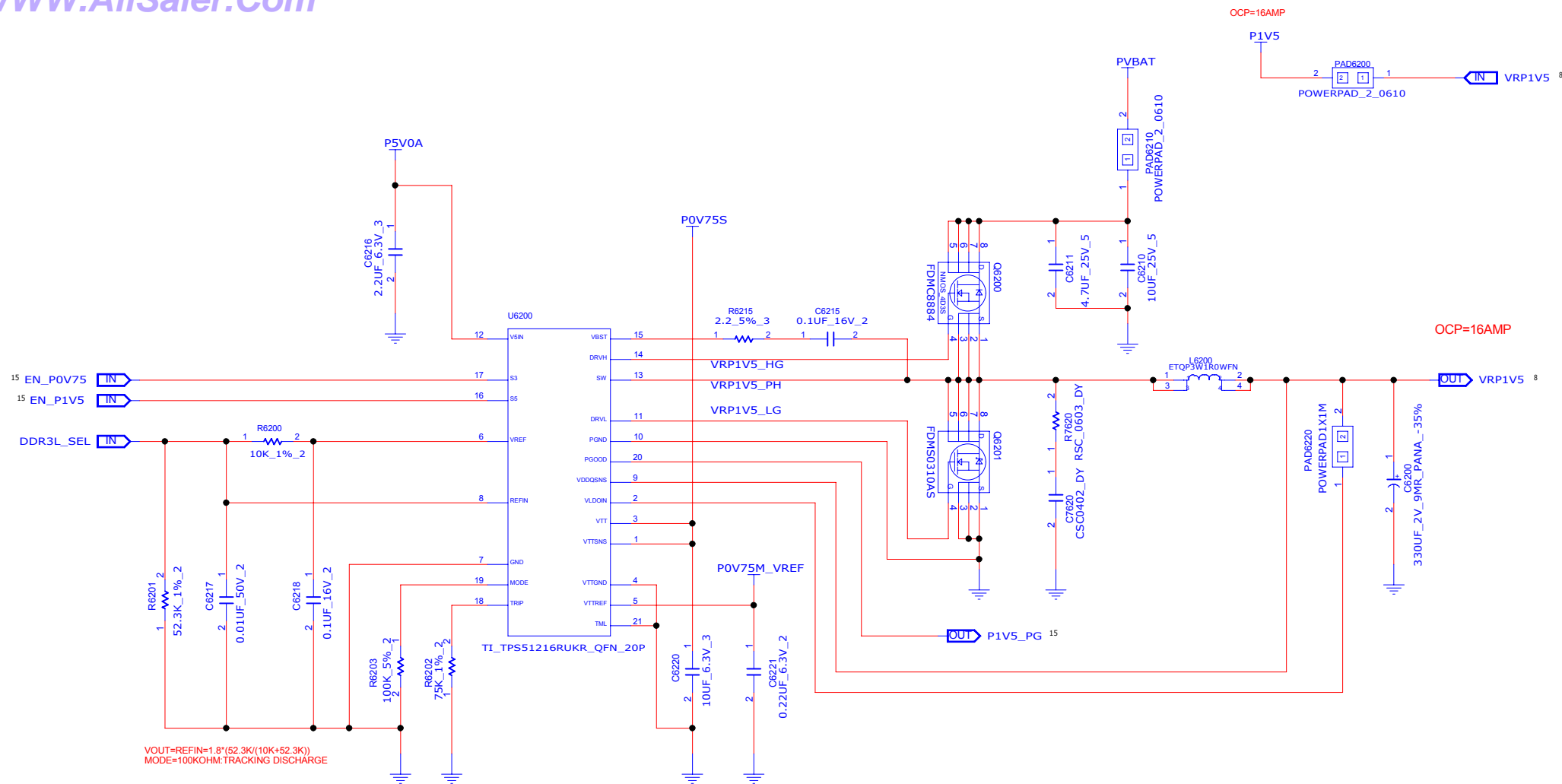


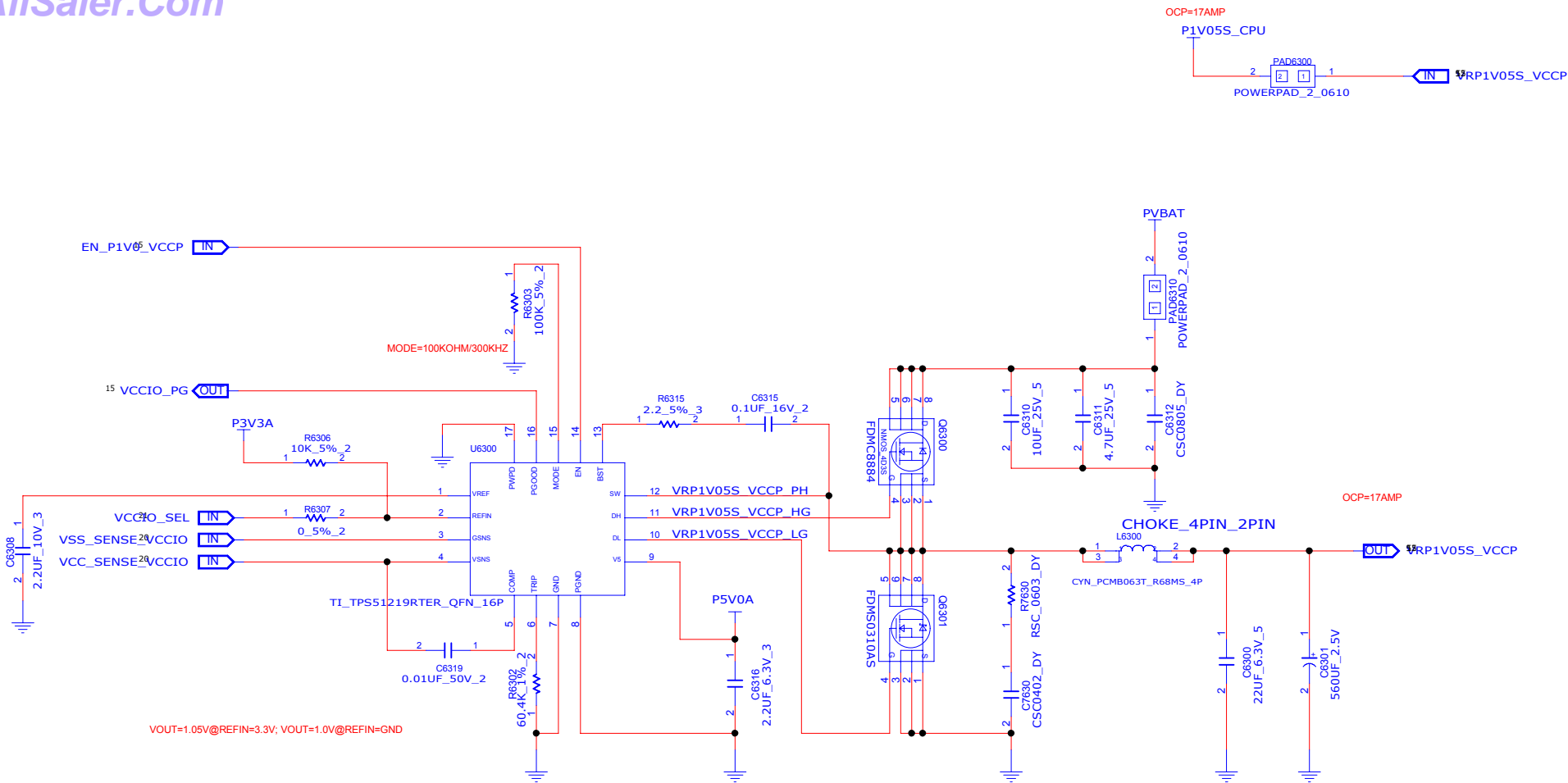
Table 1. S3/S5 Power State Control

STATE	S3	S5	VREF	VDDQ	VTTREF	VTT
S0	HI	HI	ON	ON	ON	ON
S3	LO	HI	ON	ON	ON	OFF(High-Z)
S4/S5	LO	LO	OFF	OFF(Discharge)	OFF(Discharge)	OFF(Discharge)

INVENTEC

TITLE MODEL,PROJECT,FUNCTION P1V5			
SIZE A3	CODE CS	DOC NUMBER 1310xxxxx-0-0	REV X01
SHEET 8 of 57			

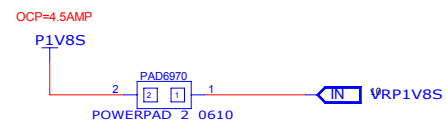
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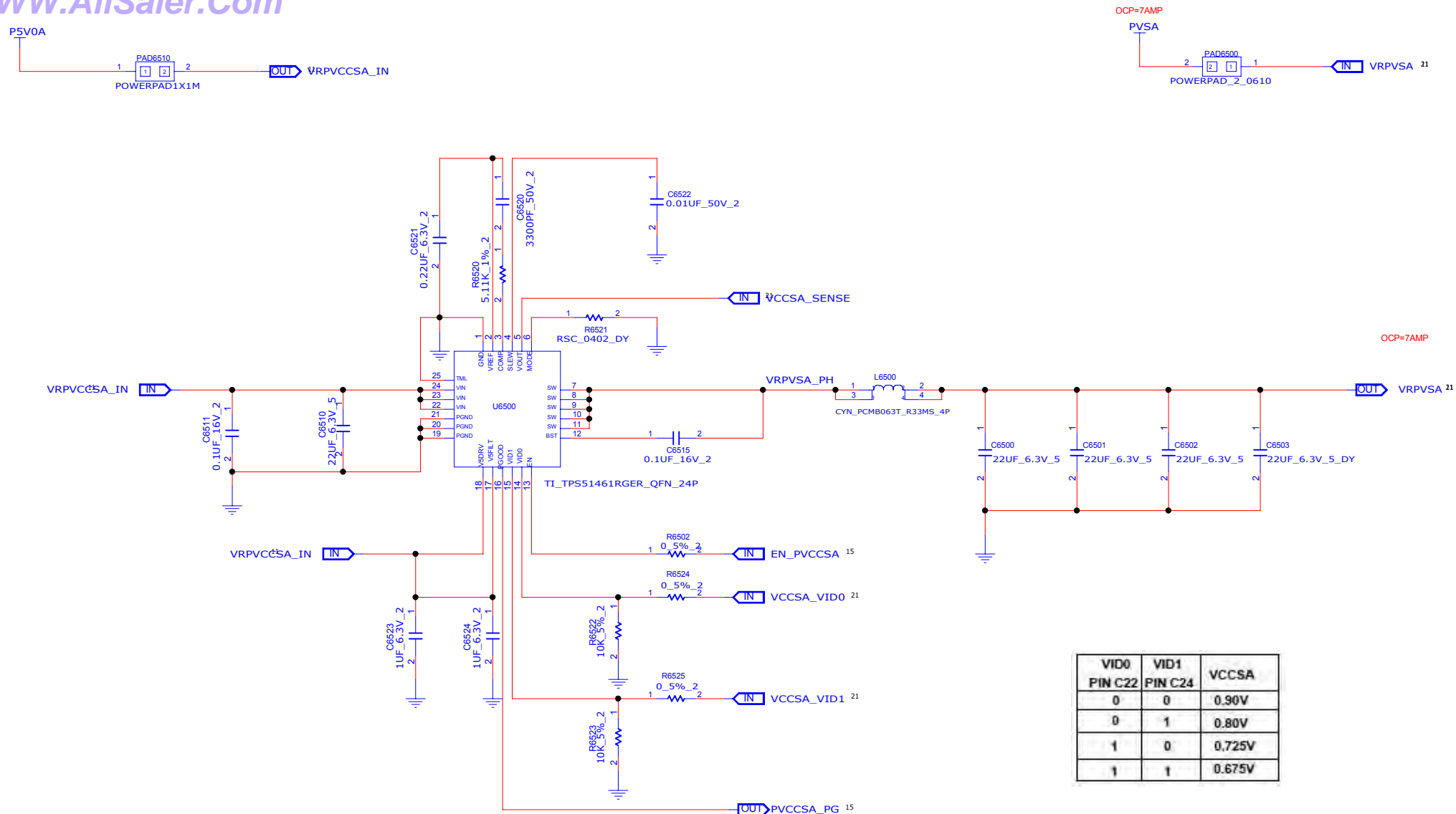
INVENTEC

TITLE			
MODEL_PROJECT,FUNCTION			
P1V05S_CPU			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxxx-0-0	X01

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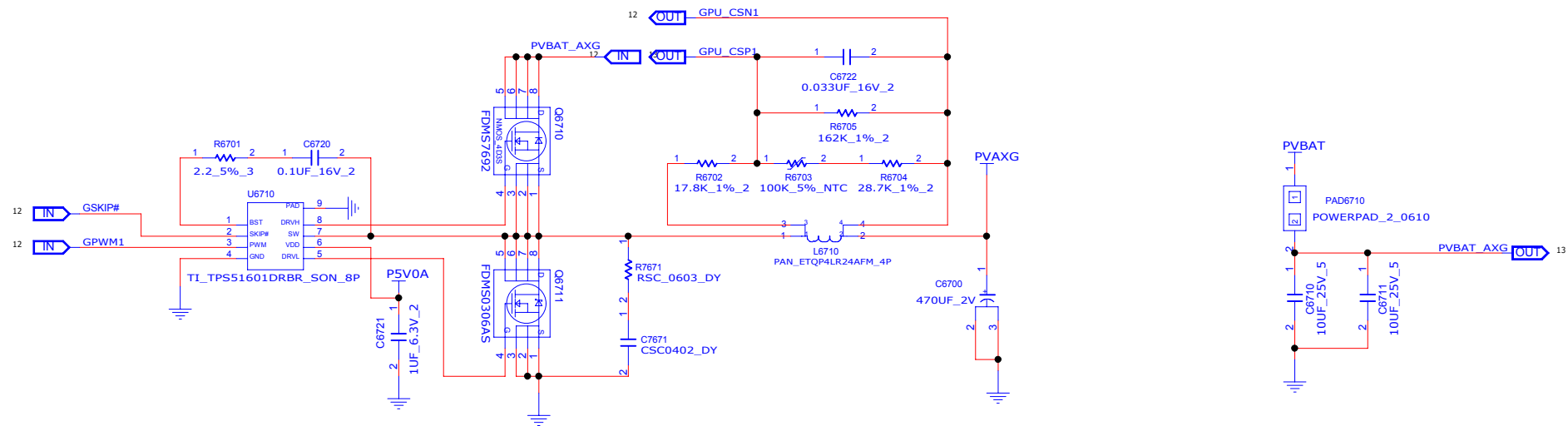
VID0 PIN C22	VID1 PIN C24	VCCSA
0	0	0.90V
0	1	0.80V
1	0	0.725V
1	1	0.675V

R6638 

8



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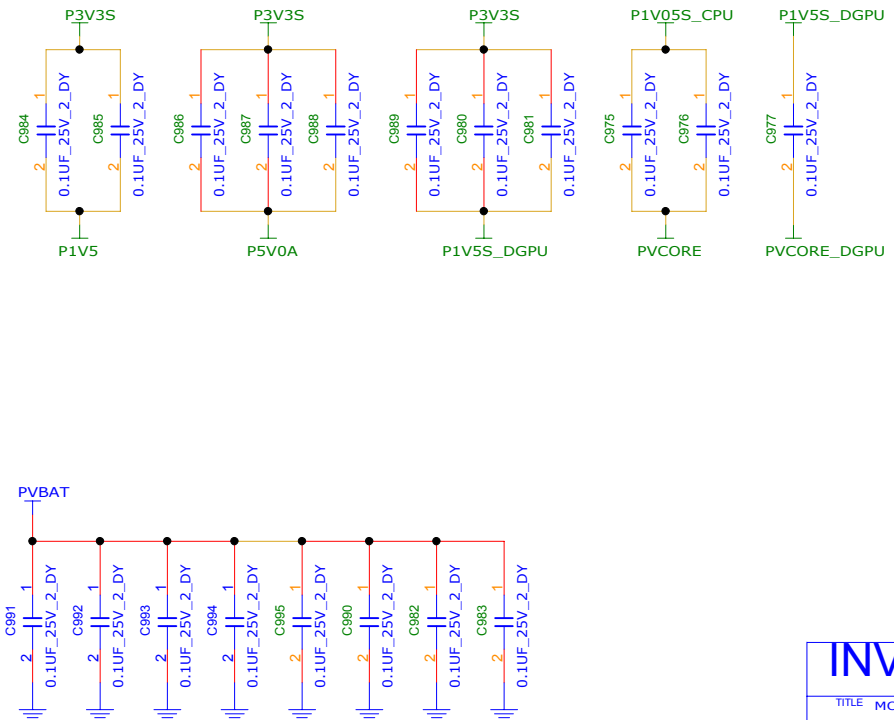
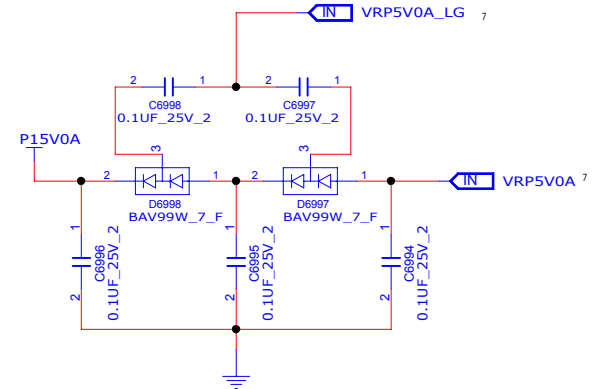
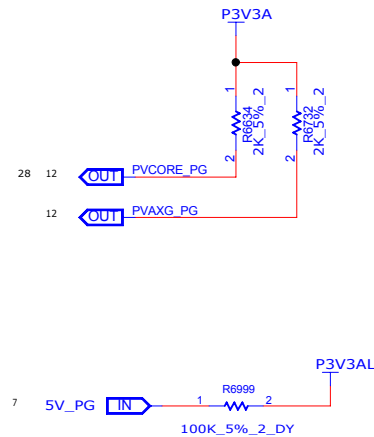


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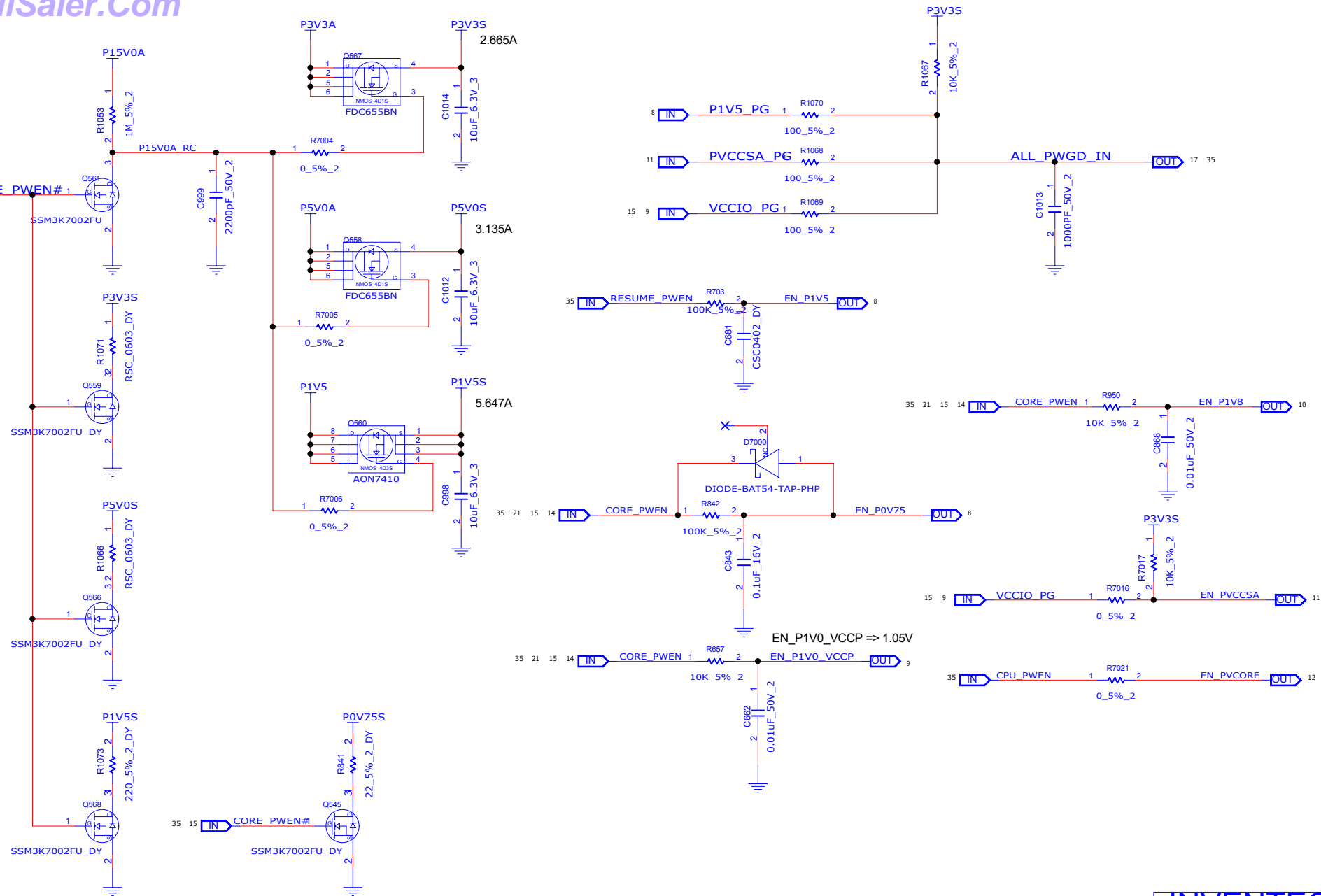
TITLE			
MODEL PROJECT,FUNCTION			
PVCORE_2			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxxx-0-0	X01

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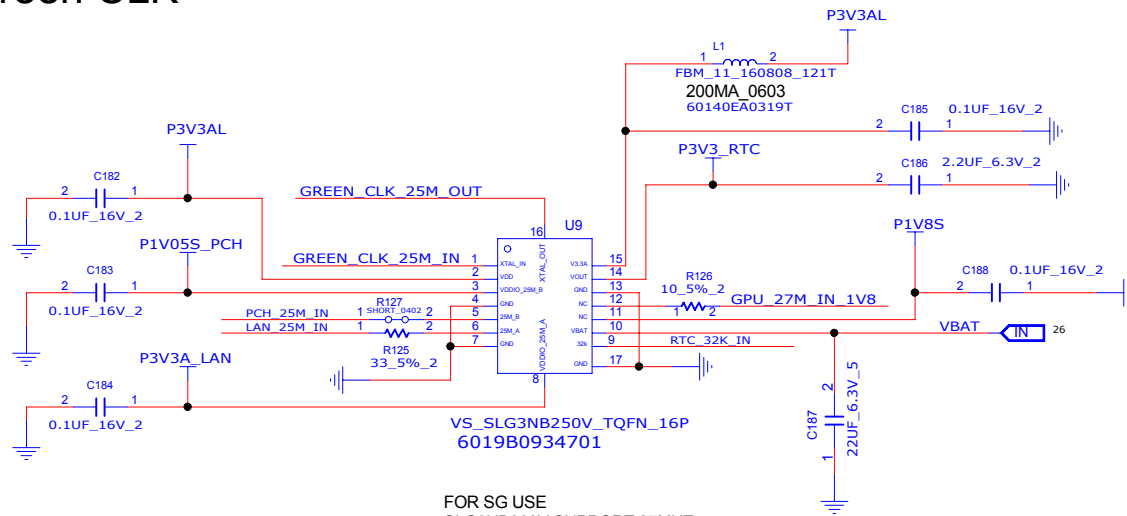


INVENTEC

TITLE			
MODEL PROJECT FUNCTION			
P3V0S & P3V3S			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxxx-0-0	X01

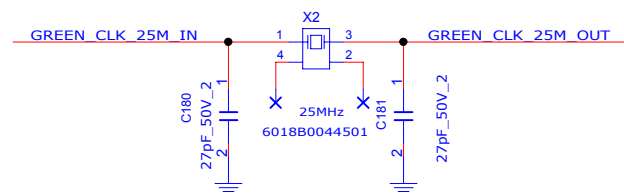
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FOR SG USE
SLG3NB300V SUPPORT 27MHZ
P/N : 6019B0941101

FOR UMA USE
SLG3NB250V
P/N : 6019B0934701



FOR UMA
U9 : 6019B0934701
R126 : NA
C188 : NA

FOR SG
U9 : SLG3NB300V P/N APPLY
R126 : MOUNT
C188 : MOUNT

IF USE U9 (SLG3NB250V)

MOUNT

X2

R1

L1

C18

C18

C18
C18C18
C18C18
C18C18
C18

C18

R90

R91

R4'

R12

OPEN (PAGE26)

D4400

X501

R958

C874

C875

OPEN (PAGE27)

X503

R1041

C962

C965

INVENTEC

TITLE	MODEL,PROJECT,FUNCTION

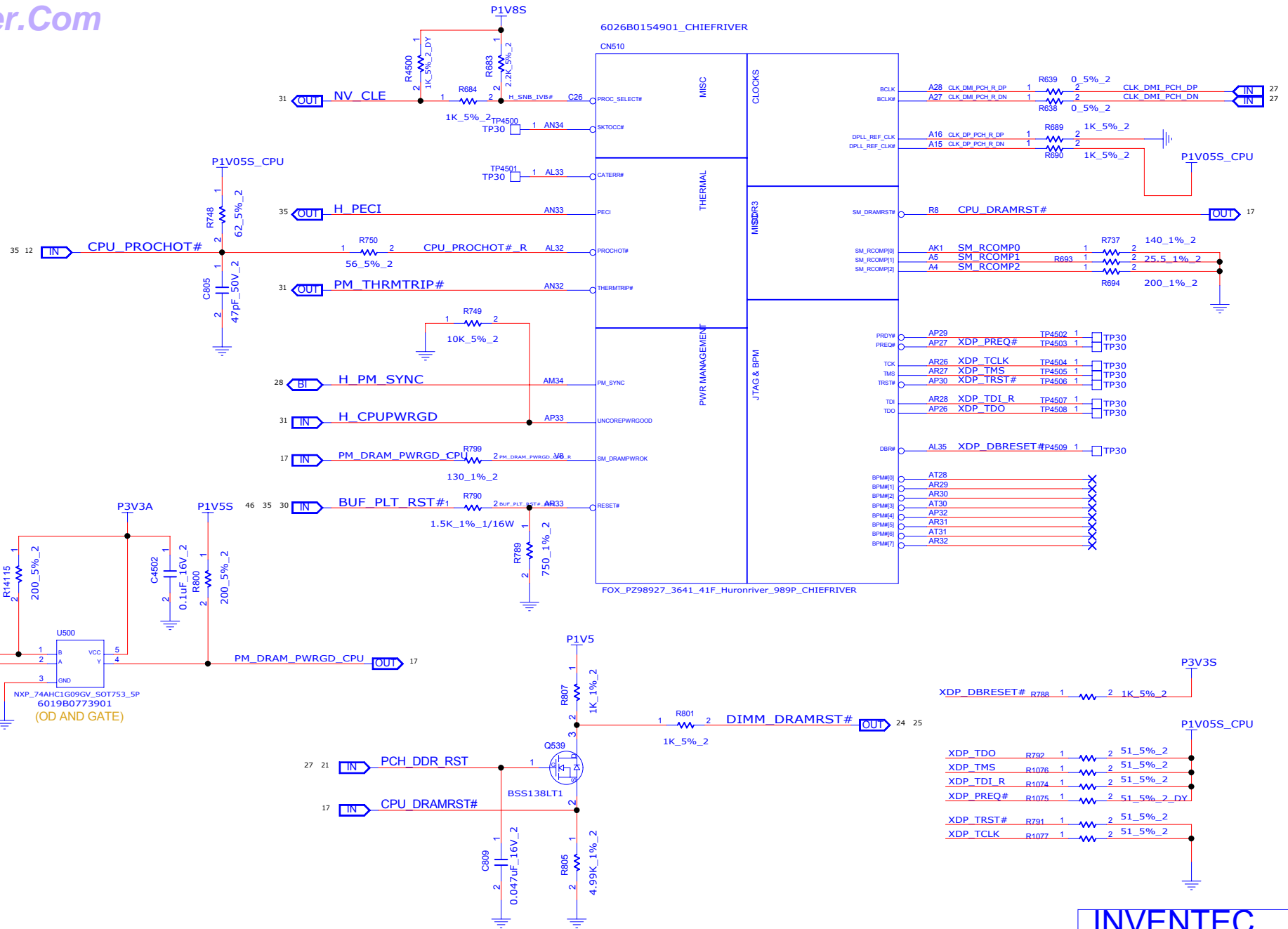
GREEN CLK SLG3NB250

SIZE A2	CODE CE	DOC.NUMBER 1310xxxxx-0-0
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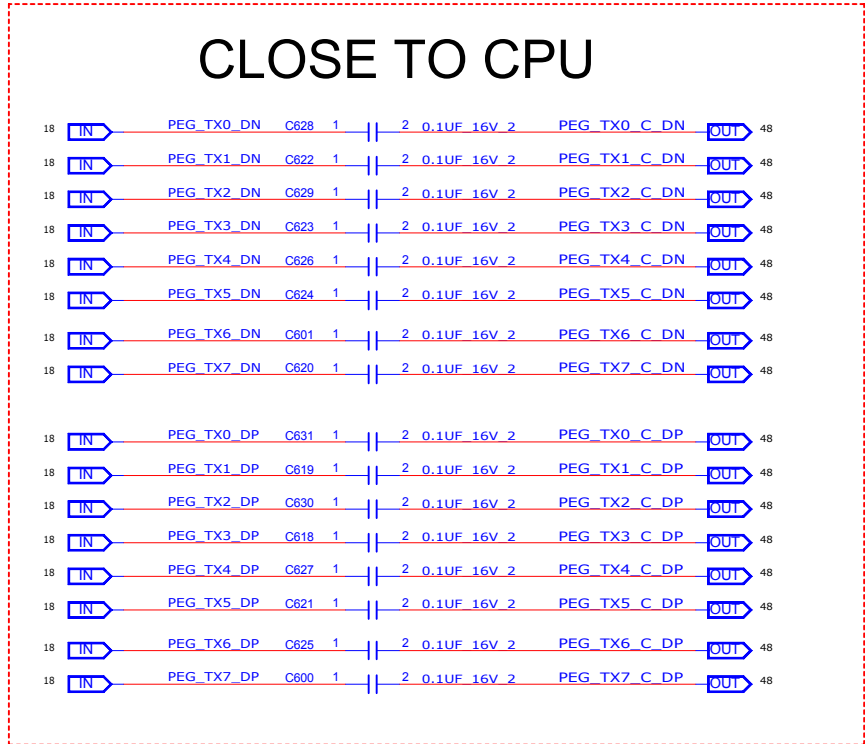
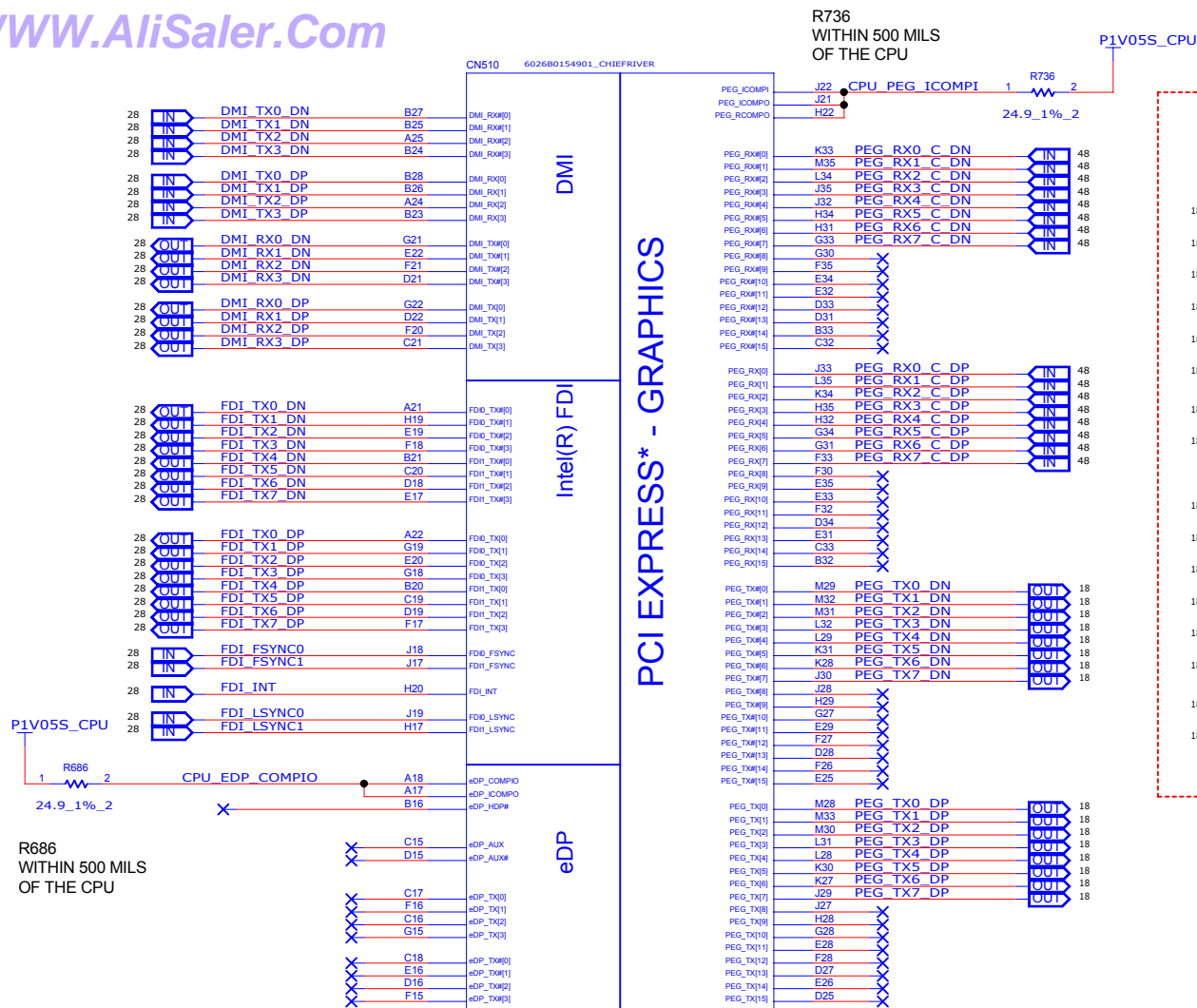
REV
X01

CHANGE by	XXX	DATE	21-OCT-2002
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INVENTEC			
TITLE			
MODEL,PROJECT,FUNCTION			
CPU			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxxx-0-0	X01



INVENTEC

TITLE MODEL,PROJECT,FUNCTION

DOC NUMBER 1310xxxx-0-0

REV X01

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CHANGE by XXX DATE 21-OCT-2002

D

C

B

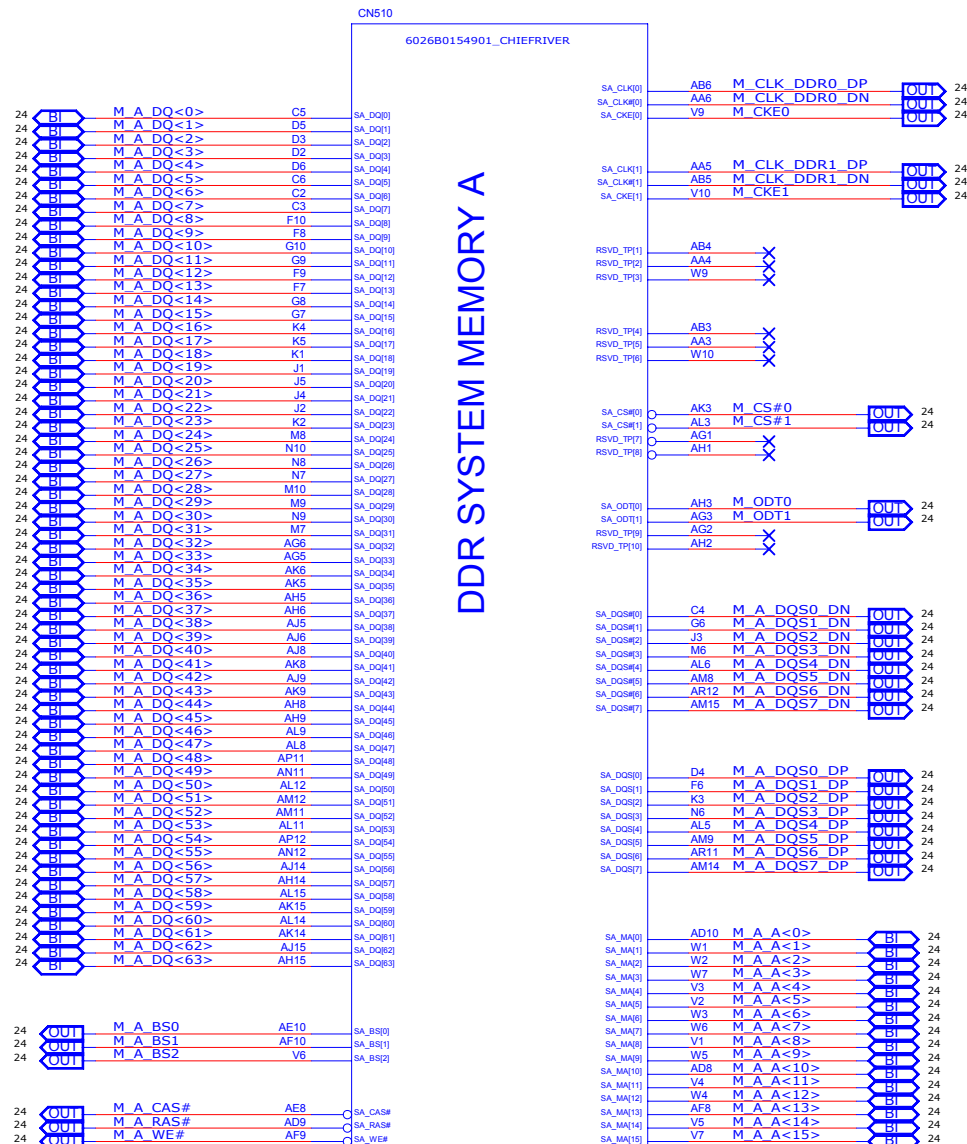
A

D

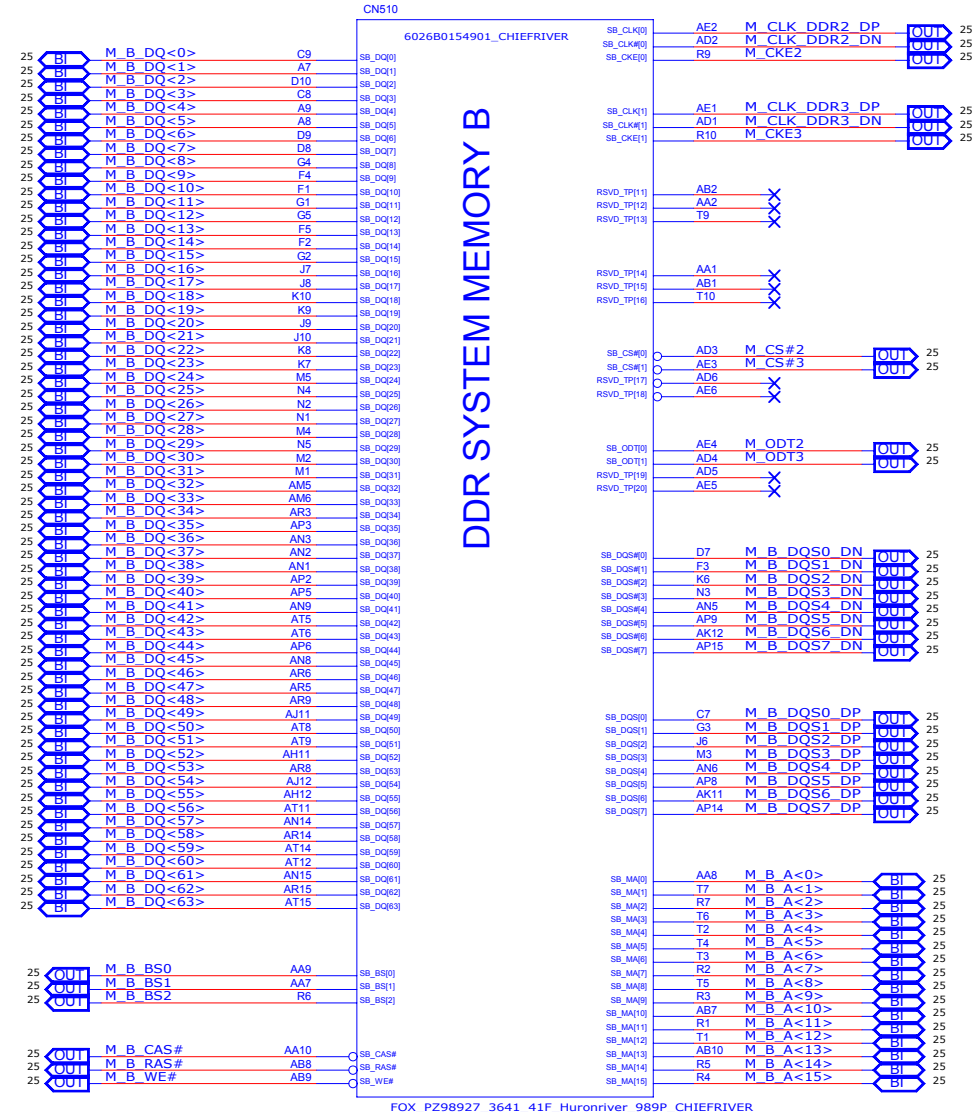
C

B

A



FOX_PZ98927_3641_41F_Huronriver_989P_CHIEFRIVER



FOX_PZ98927_3641_41F_Huronriver_989P_CHIEFRIVER

INVENTEC

TITLE			
MODEL,PROJECT,FUNCTION			
CPU - 3			
SIZE A3	CODE CS	DOC NUMBER 1310xxxxx-0-0	REV X01
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CHANGE by XXX DATE 21-OCT-2002

IMAX = 53A

PVCORE

CN510

6026B0154901_CHIEFRIVER

POWER

PEG AND DDR

CORE SUPPLY

SVID

SENSE LINES

P1V05S_CPU

VTT

VCCIO IMAX = 8.5A

P1V05S_CPU

P1V05S_CPU

PVCORE

P1V05S_CPU

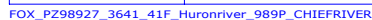
VCC_SENSE VCCIO
VSS_SENSE VCCIO

INVENTEC

TITLE			
MODEL,PROJECT,FUNCTION			
CPU - 4			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxxx-0-0	X01
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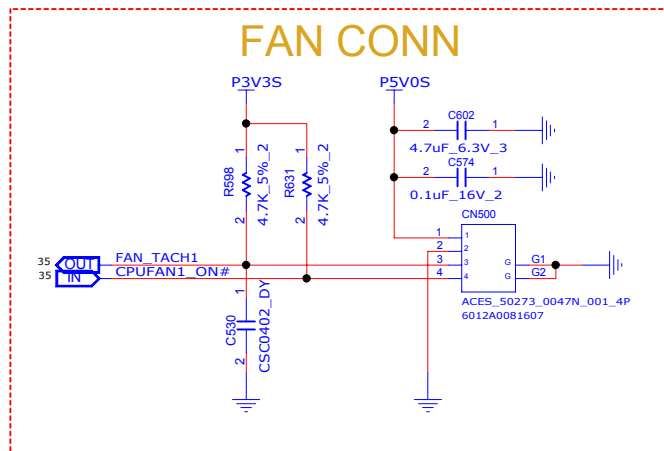
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FOX_P298927_3641_41F_Huronriver_989P_CHIEFRIVER



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7.2.5 Register : Local Diode Temperature Alert Temperature

Location : Read = Address 05h Write = Address 0Bh

Type : Read/Write

Power on default value : Hardware strapping

BIT	7	6	5	4	3	2	1	0
NAME	LDT Alert Temperature							
VALUE	SIGN	B4	32	16	8	4	2	1
DEFAULT	Hardware strapping							

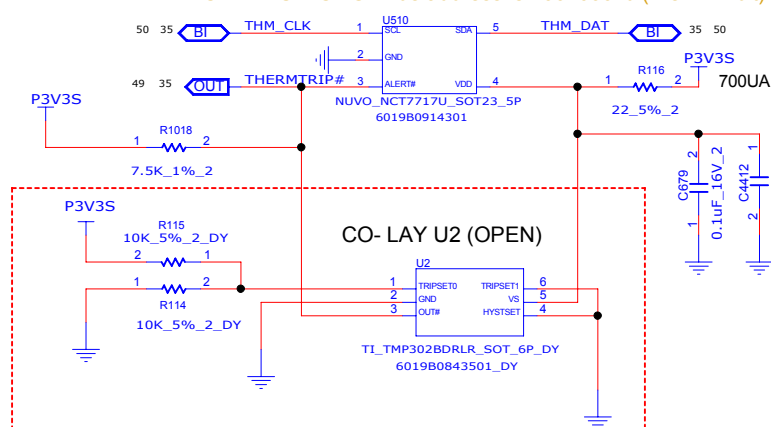
6.6 ALERT# point hardware power-on setting

The default value could be set after power up 100ms by different pull-up resistor of ALERT# pin:

PULL-UP RESISTOR	TEMPERATURE (°C)
2KΩ	75
7.5KΩ	90
10.5KΩ	100
14KΩ	105
18.7KΩ	110

THERMAL SENSOR(LOCAL)

NCT7717U I2C / SMBus address is 1001000xb (x is R/W bit).



TMP302 Available Versions⁽¹⁾

DEVICE	SELECTABLE TRIP POINTS (°C)
TMP302A	50, 55, 60, 65
TMP302B	70, 75, 80, 85
TMP302C	90, 95, 100, 105
TMP302D	110, 115, 120, 125

(1) For other available trip points, please contact a TI representative.

Table 1. Trip Point vs TRIP_{SET1} and TRIP_{SET0}

TRIP _{SET1}	TRIP _{SET0}	TMP302A	TMP302B	TMP302C	TMP302D
GND	GND	+50°C	+70°C	+90°C	+110°C
GND	V _S	+55°C	+75°C	+95°C	+115°C
V _S	GND	+60°C	+80°C	+100°C	+120°C
V _S	V _S	+65°C	+85°C	+105°C	+125°C

Table 2. HYST_{SET} Window

HYST _{SET}	THRESHOLD HYSTERESIS
GND	+5°C
V _S	+10°C

INVENTEC

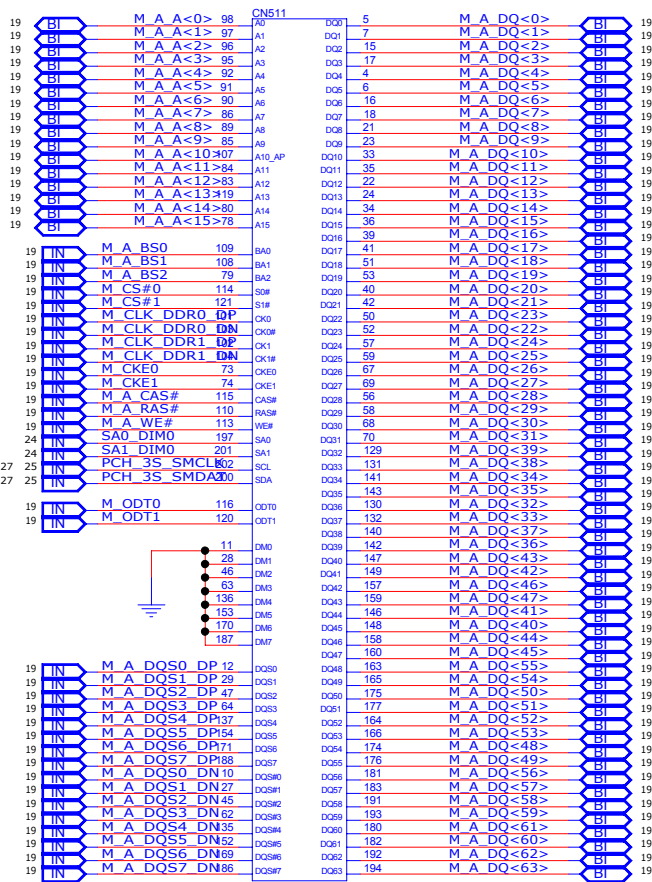
TITLE
MODEL PROJECT,FUNCTION
THERMAL & FAN

SIZE A3	CODE CS	DOC NUMBER 1310xxxxx-0-0	REV X01
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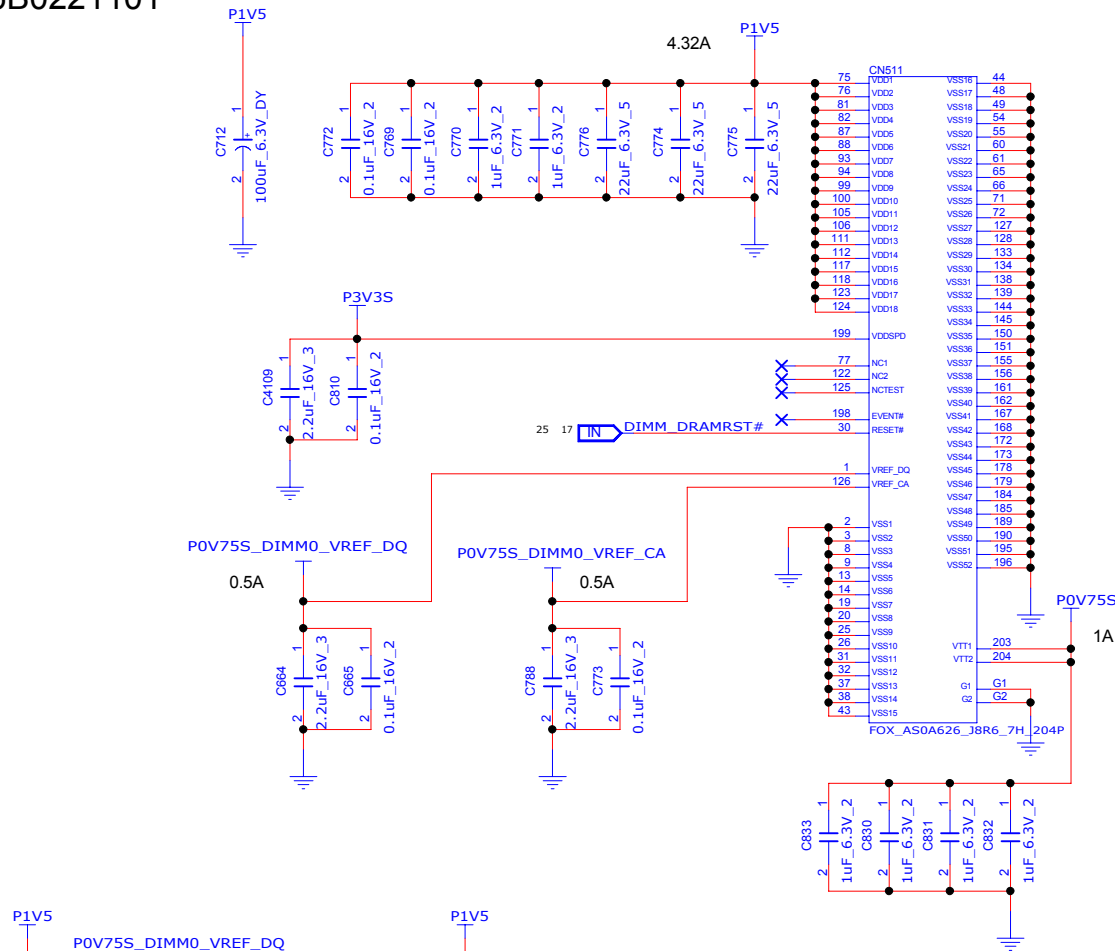
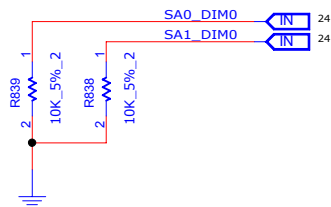
CHANGE by XXX DATE 21-OCT-2002

DDR3 (8mm) P/N : 6026B0221101



FOX_AS0A626_J8R6_7H_204P

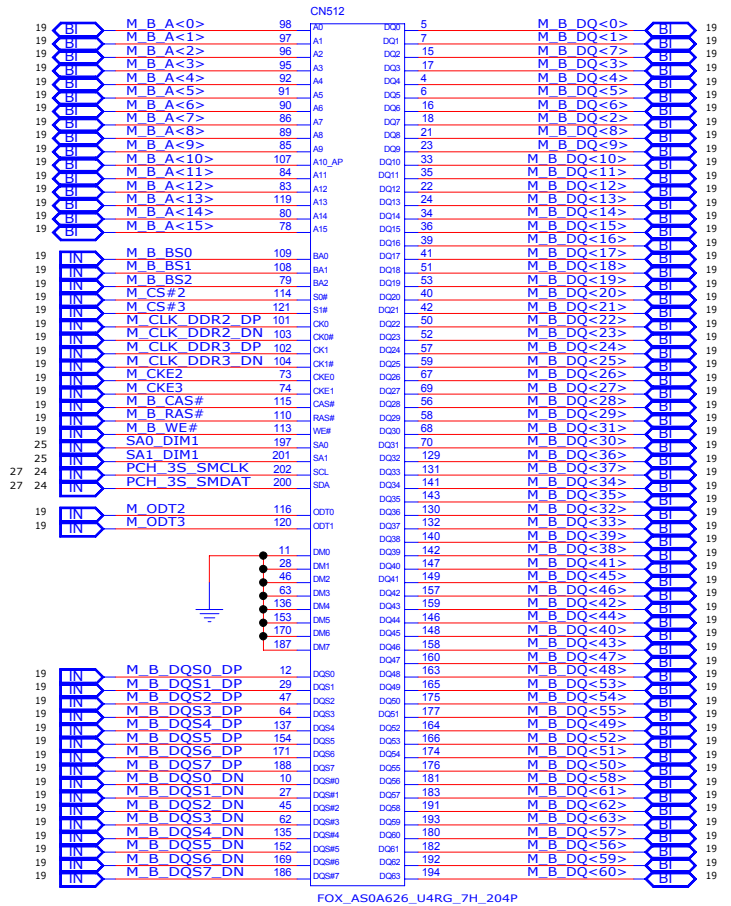
Note :
SO-DIMMA SPD Address is 0xA0
SO-DIMMA TS Address is 0x30



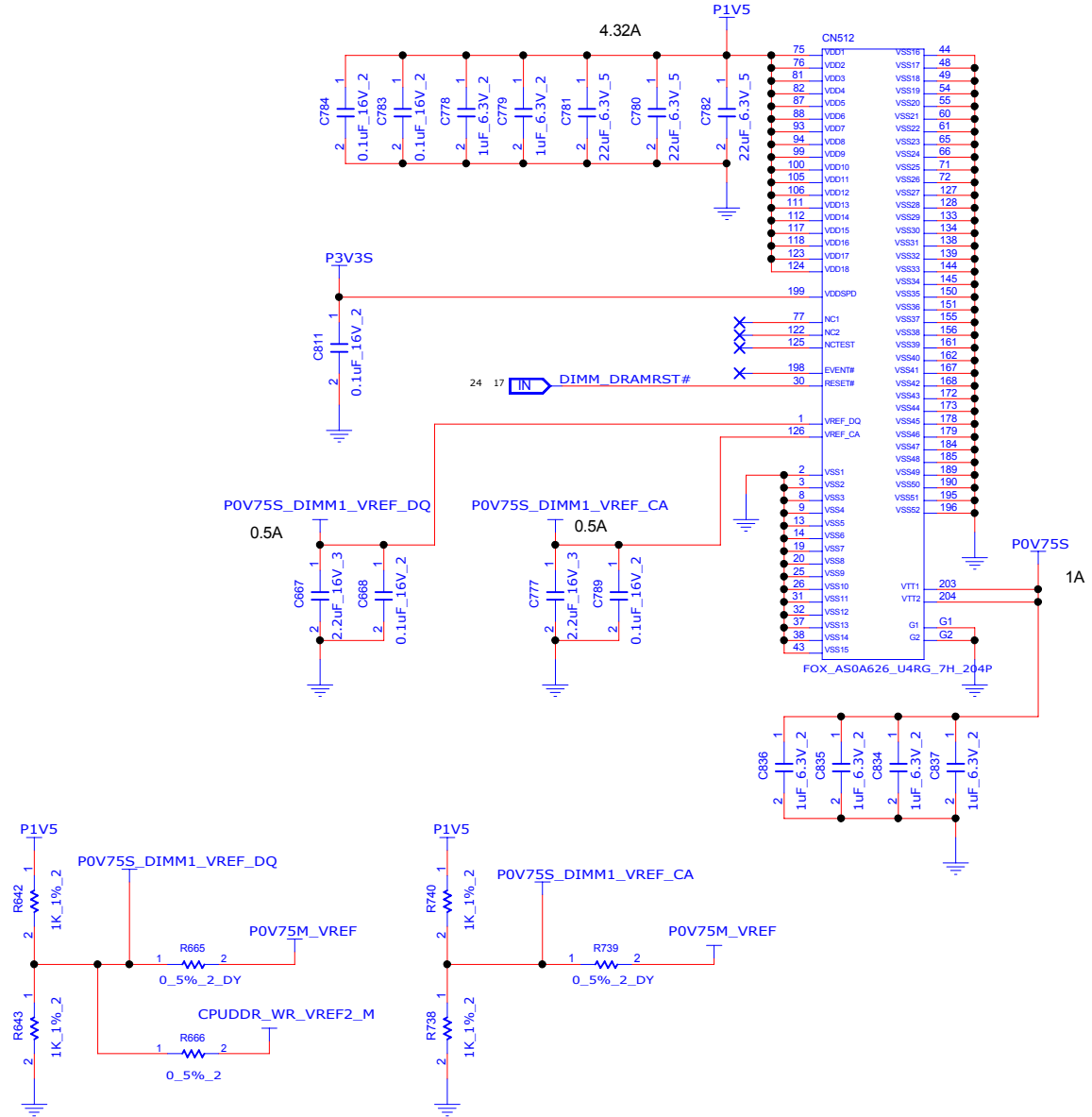
SANDY BRIDGE + IVY BRIDGE DG4.14

INVENTEC

TITLE			
MODEL,PROJECT,FUNCTION			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxxx-0-0	X01



Note :
SO-DIMMA SPD Address is 0xA4
SO-DIMMA TS Address is 0x34

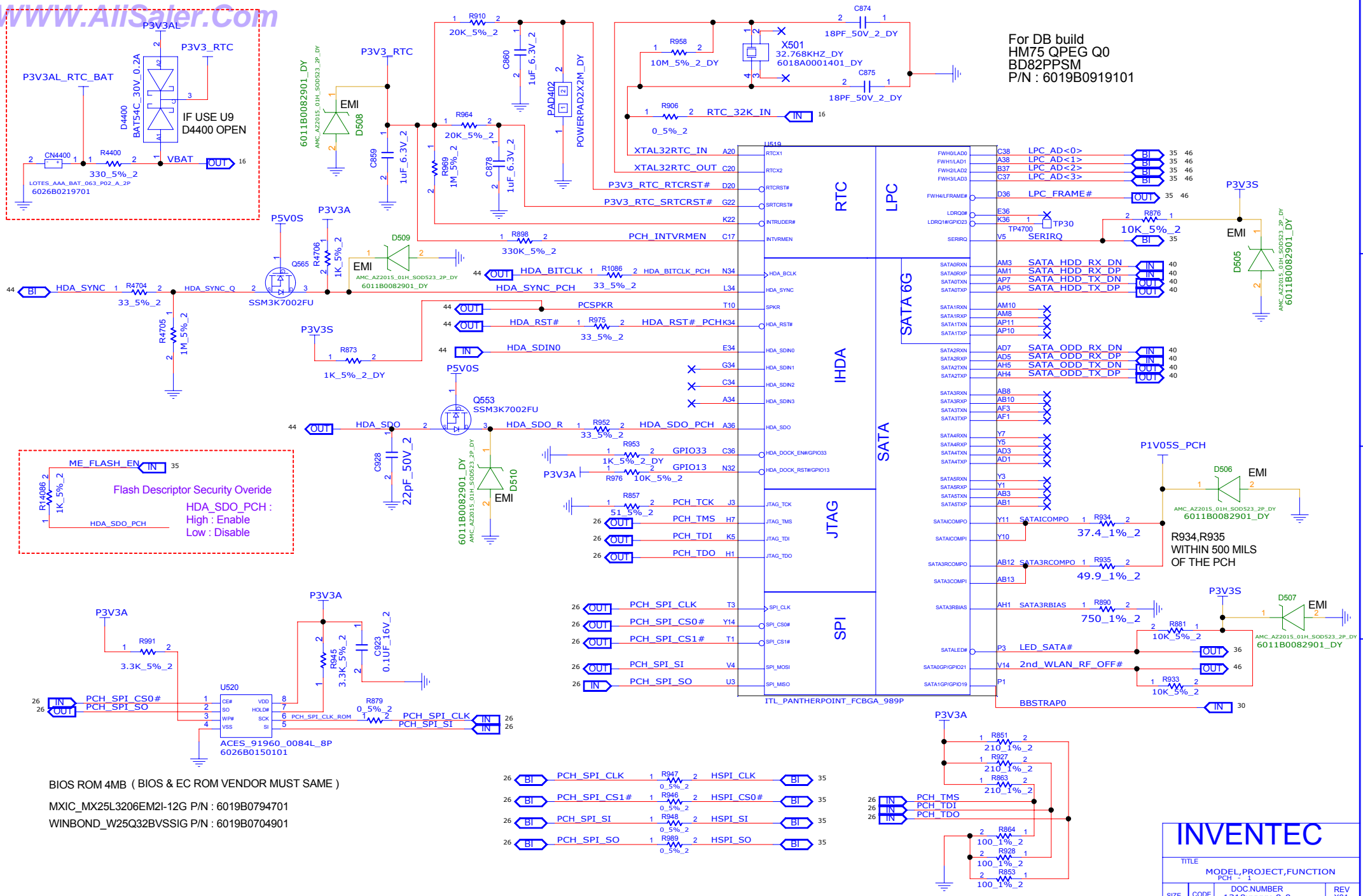


SANDY BRIDGE + IVY BRIDGE DG4.14

INVENTEC

TITLE			
MODEL,PROJECT,FUNCTION			
DDR3			
SIZE	CODE	DOCNUMBER	REV
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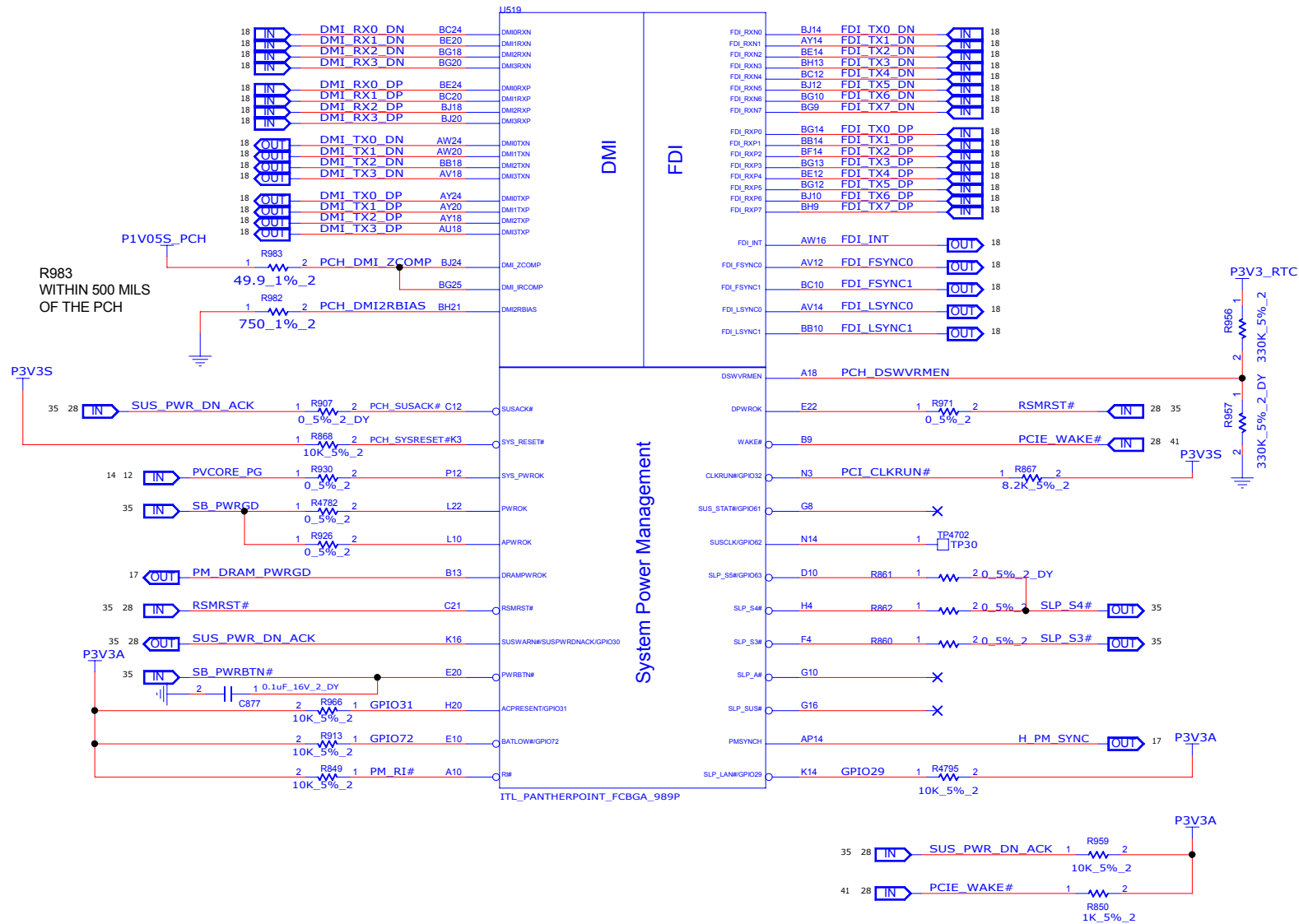
For DB build
HM75 QPEG Q0
BD82PPSM
P/N : 6019B0919101



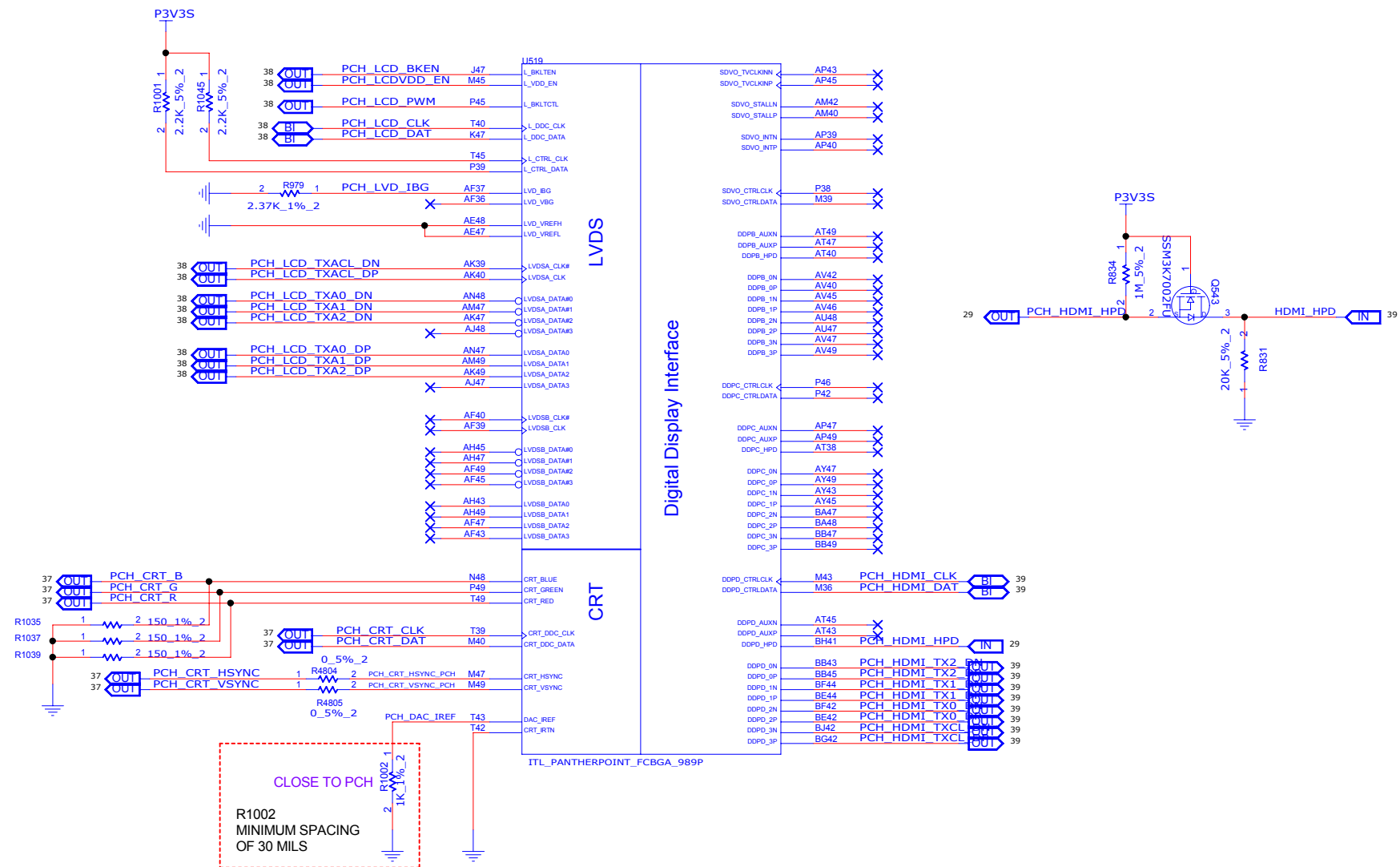
INVENTEC

TITLE			
MODEL,PROJECT,FUNCTION			
PCH 1			
SIZE	CODE	DOC NUMBER	REV
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INVENTEC				
TITLE				
MODEL,PROJECT,FUNCTION				
PCH - 3				
SIZE	CODE	DOC NUMBER	REV	
A3	CS	1310xxxxx-0-0	X01	

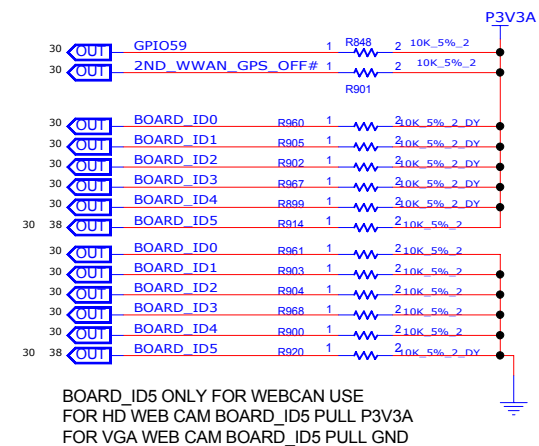
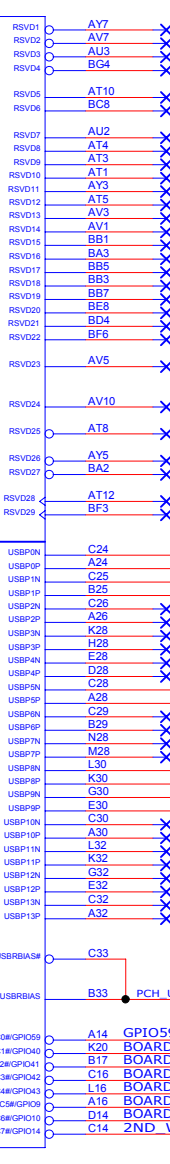
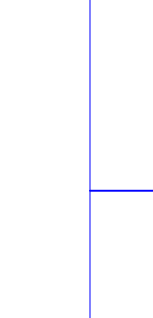
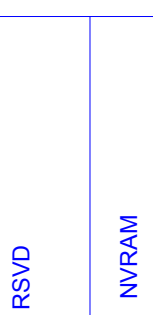
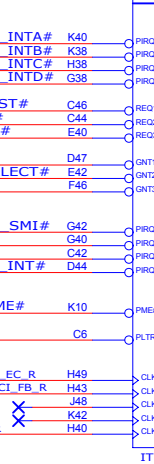
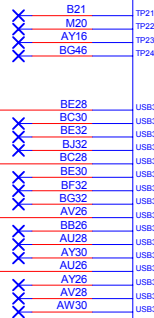
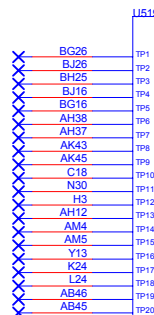
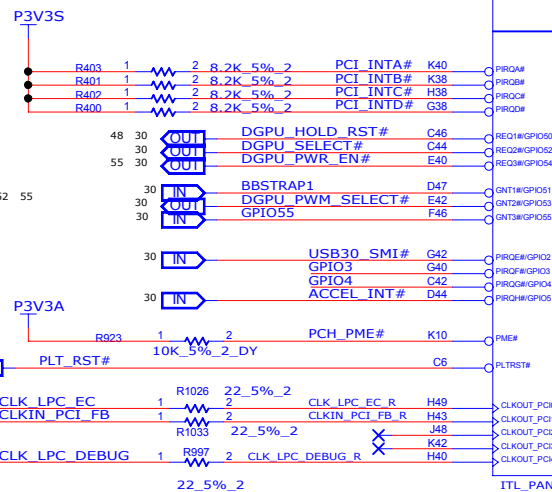
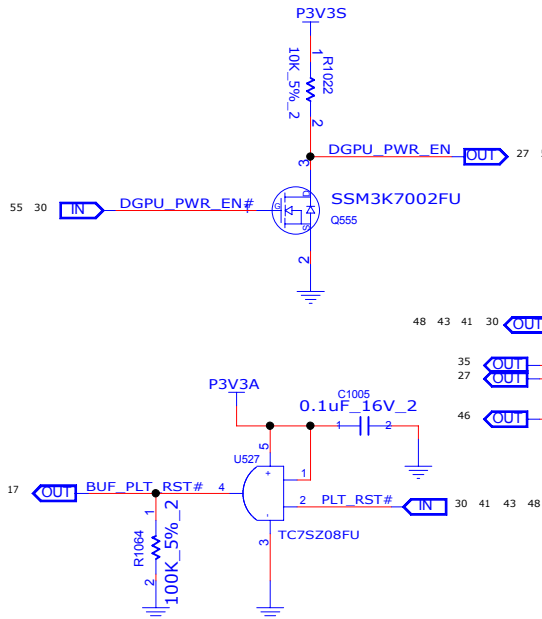
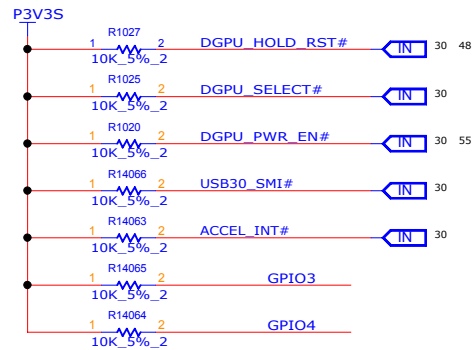
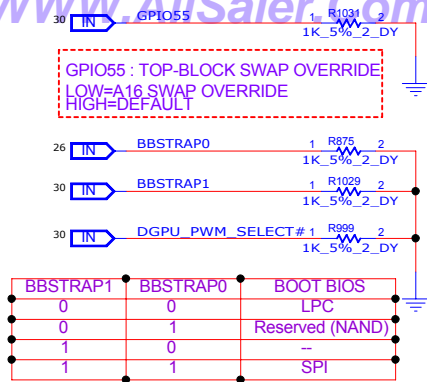


INVENTEC

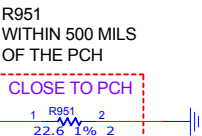
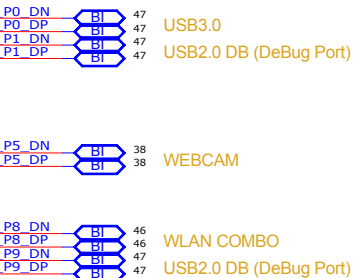
TITLE
MODEL,PROJECT,FUNCTION
PCH - 4

SIZE CODE DOC NUMBER REV
A3 CS 1310xxxx-0-0 X01

CHANGE by XXX DATE 21-OCT-2002 SHEET 29 of 57



	ID5	ID4	ID3	ID2	ID1	ID0
UMA-INTEL	1	0	0	0	0	0
SG-SEYMOUR	1	0	0	0	0	1
SG-THAMES	1	0	0	0	1	0

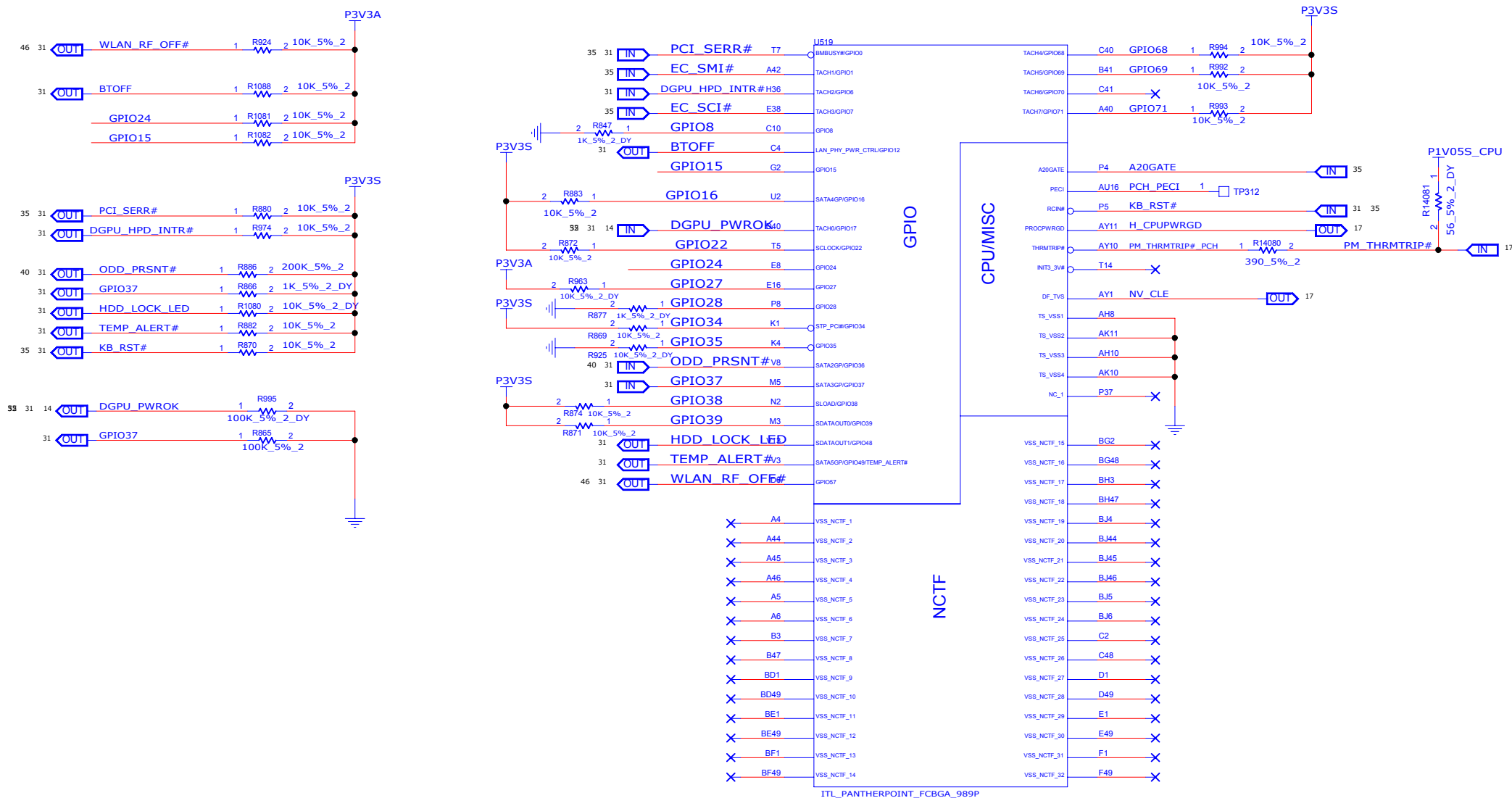


INVENTEC

TITLE
MODEL,PROJECT,FUNCTION
PCH - 5

SIZE A3 CODE CS DOC NUMBER 1310xxxx-0-0 REV X01

CHANGE by XXX DATE 21-OCT-2002 SHEET 30 of 57

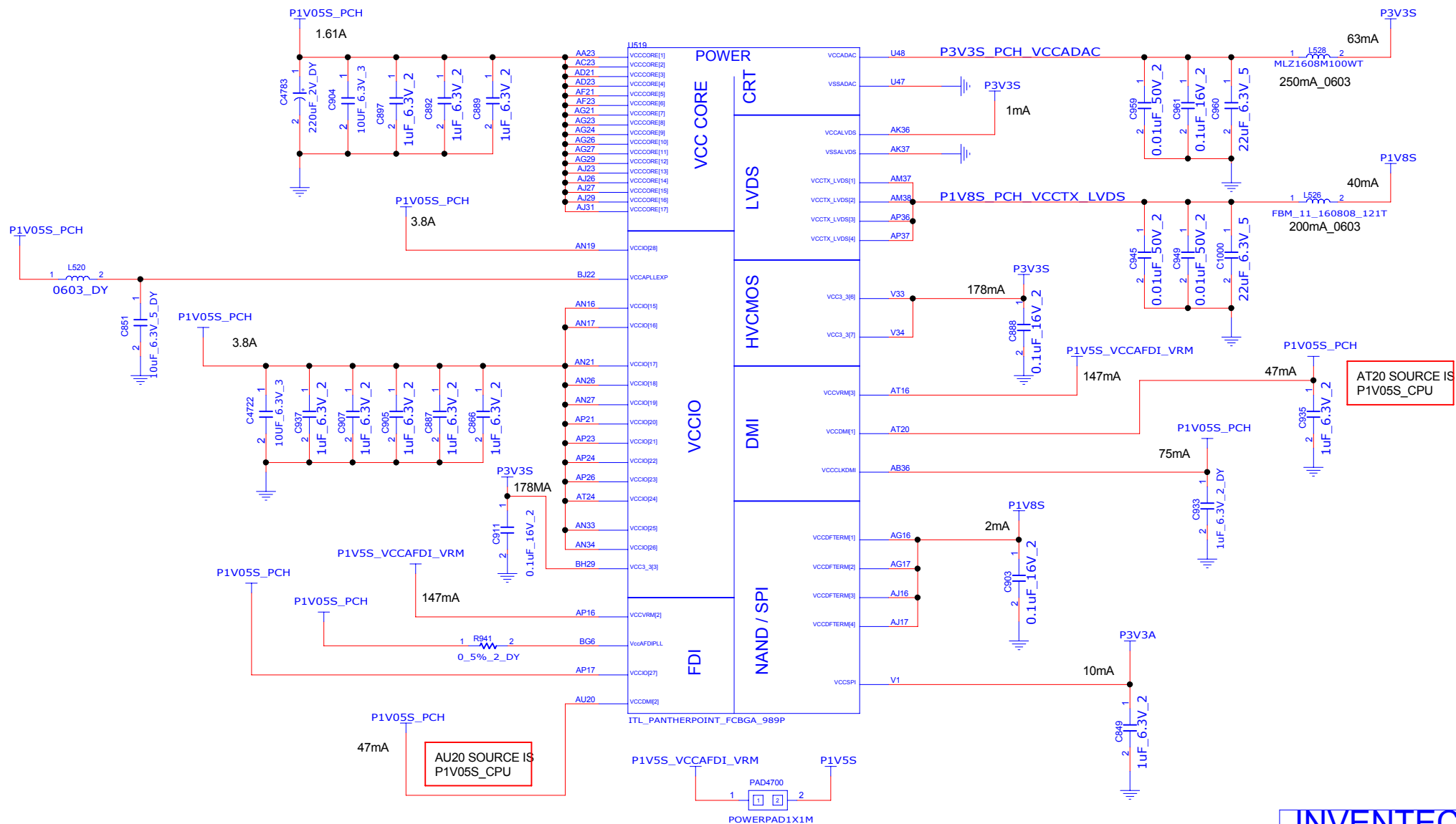


INVENTEC

TITLE			
MODEL,PROJECT,FUNCTION			
PCH - 6			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxxx-0-0	X01

CHANGE by: XXX DATE: 21-OCT-2002

SHEET 31 of 57

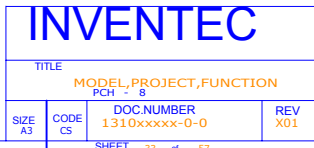


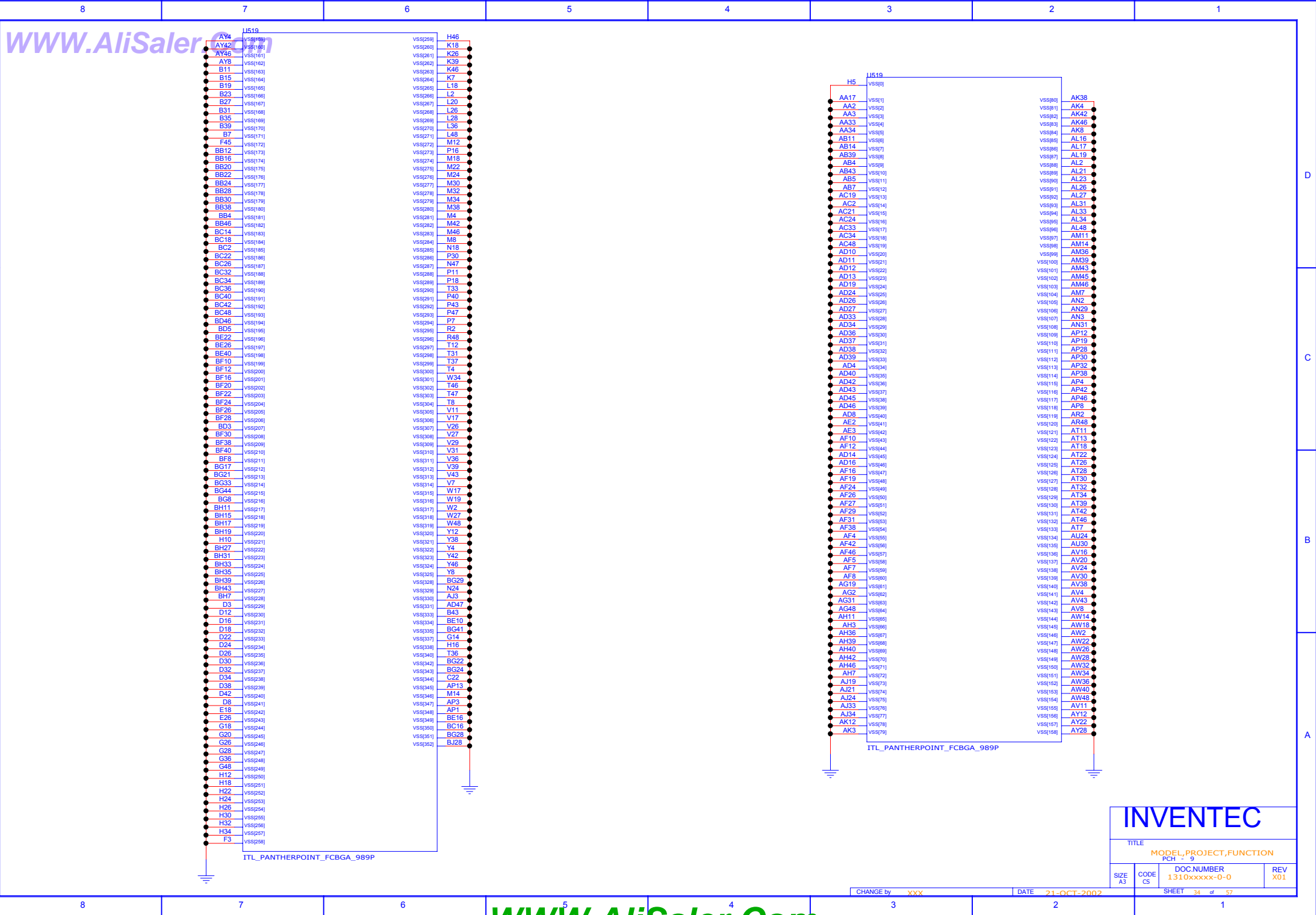
INVENTEC

TITLE			
MODEL,PROJECT,FUNCTION			
PCH - 7			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxxx-0-0	X01

CHANGE by XXX DATE 21-OCT-2002

SHEET 32 of 57



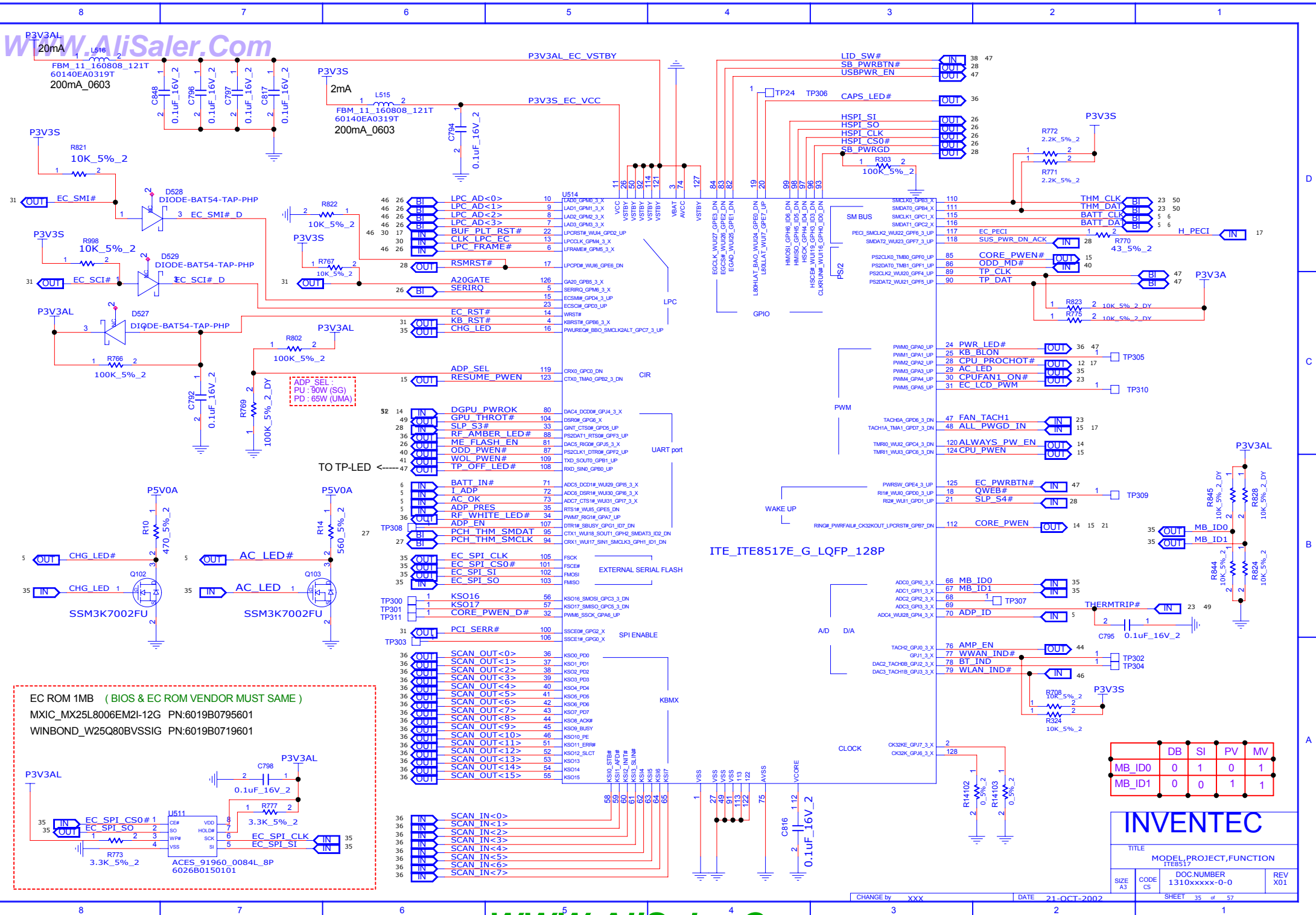


D

C

B

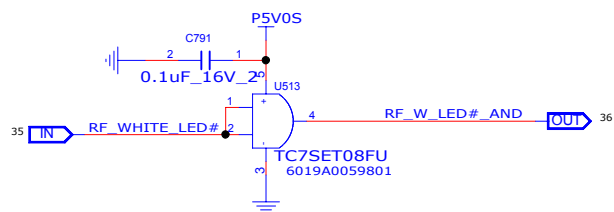
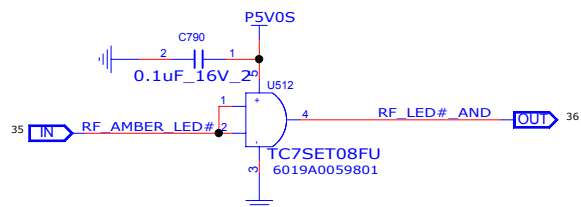
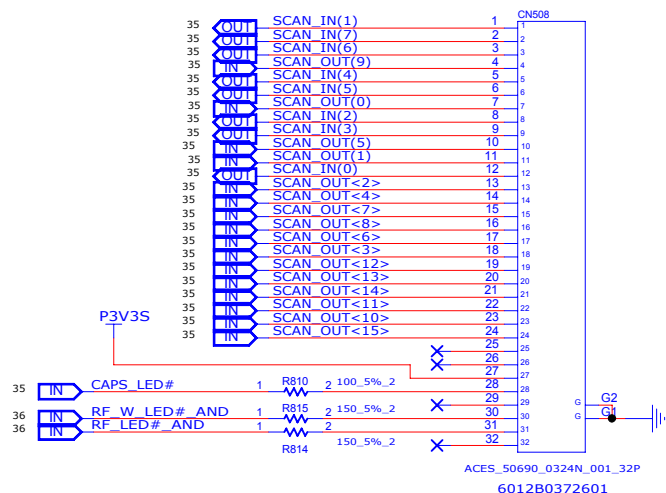
A



	DB	SI	PV	MV
MB_ID0	0	1	0	1
MB_ID1	0	0	1	1

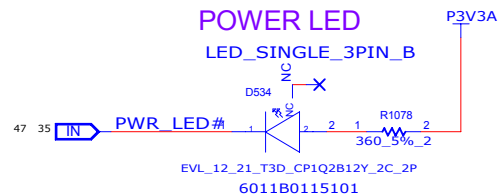
INVENTEC			
TITLE			
MODEL PROJECT,FUNCTION			
ITE8517			
SIZE CODE			
A3 CS			
DOC NUMBER			
1310xxxxx-0-0			
REV			
X01			
SHEET			
35 of 57			

KeyBoard CONN(30 pin)

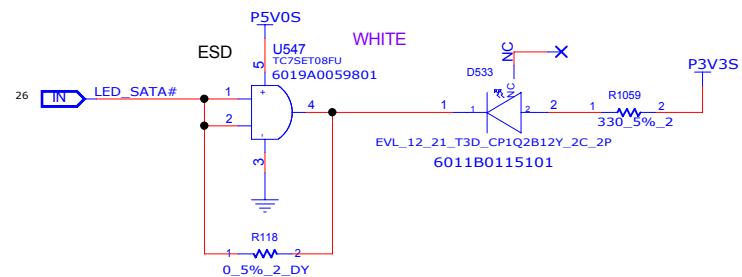


POWER LED

LED_SINGLE_3PIN_B



SATA LED & HDD HALTED LED



INVENTEC

TITLE MODEL PROJECT,FUNCTION

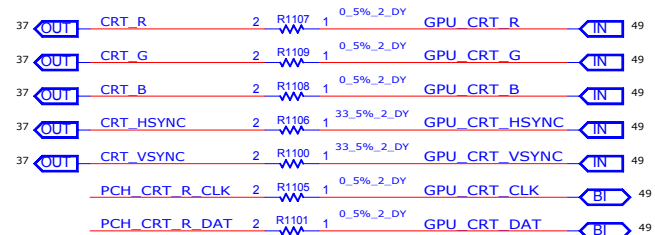
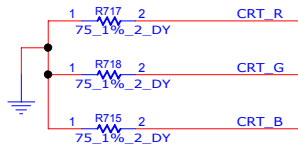
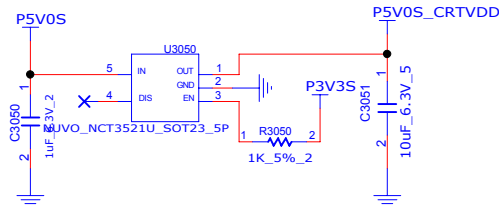
KB CONN & LED

DOC NUMBER 1310xxxxx-0-0

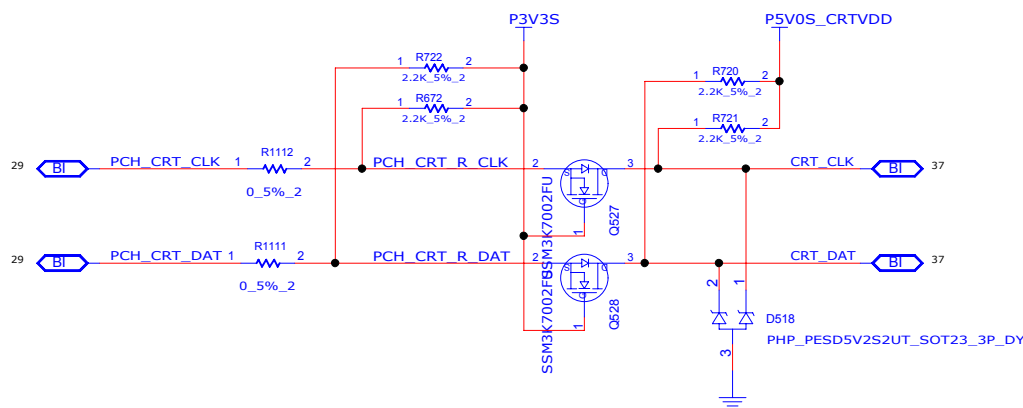
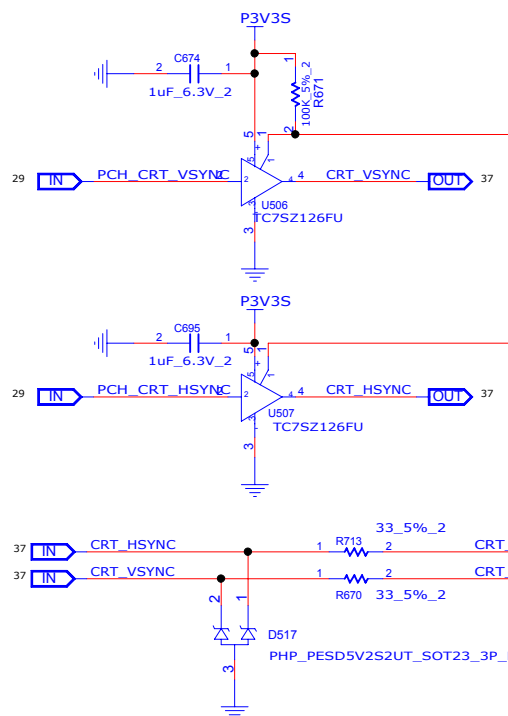
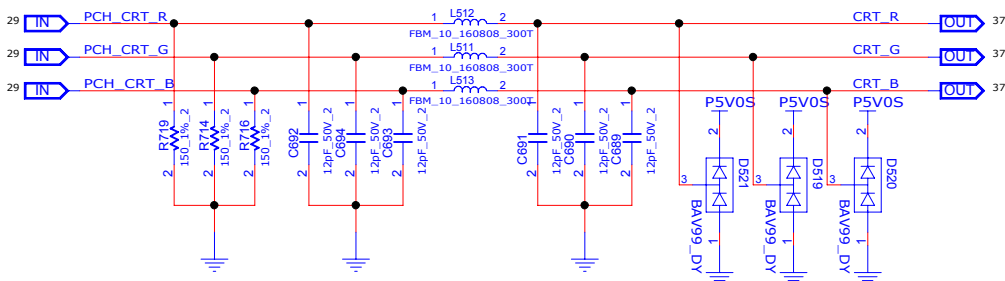
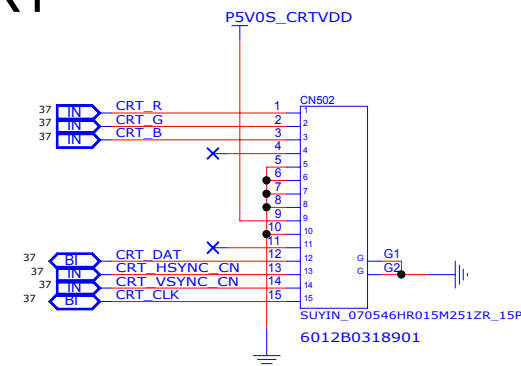
REV X01

CHANGE by XXX DATE 21-OCT-2002

SHEET 36 of 57



CRT



INVENTEC

TITLE			
MODEL PROJECT,FUNCTION			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxx-0-0	X01

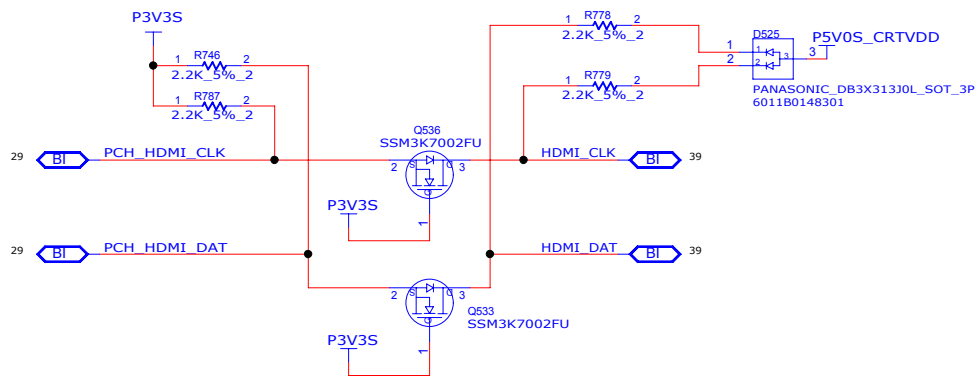
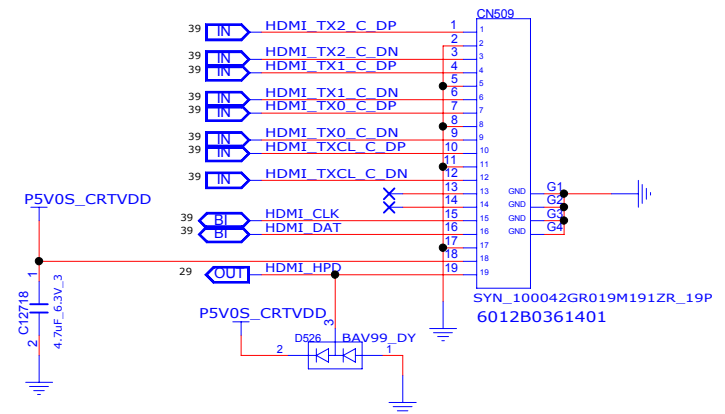
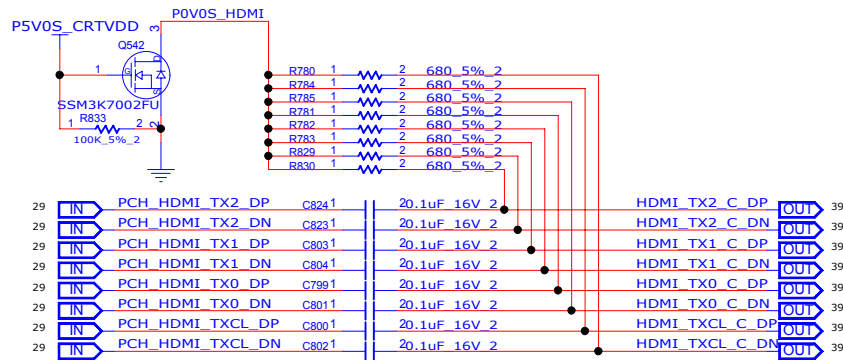
CHANGE by XXX DATE 21-OCT-2002

SHEET 37 of 57

[illegible]

CHANGE by	XXX	DATE	21-OCT-2002
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HDMI



INVENTEC

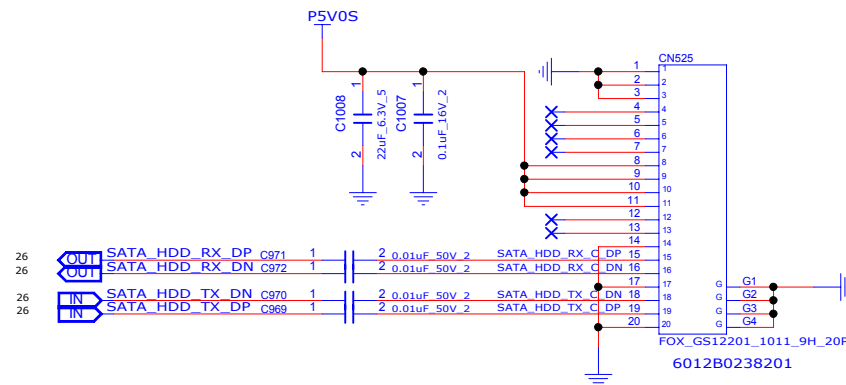
TITLE
MODEL, PROJECT, FUNCTION

SIZE A3 CODE CS DOC NUMBER 1310xxxxx-0-0 REV X01

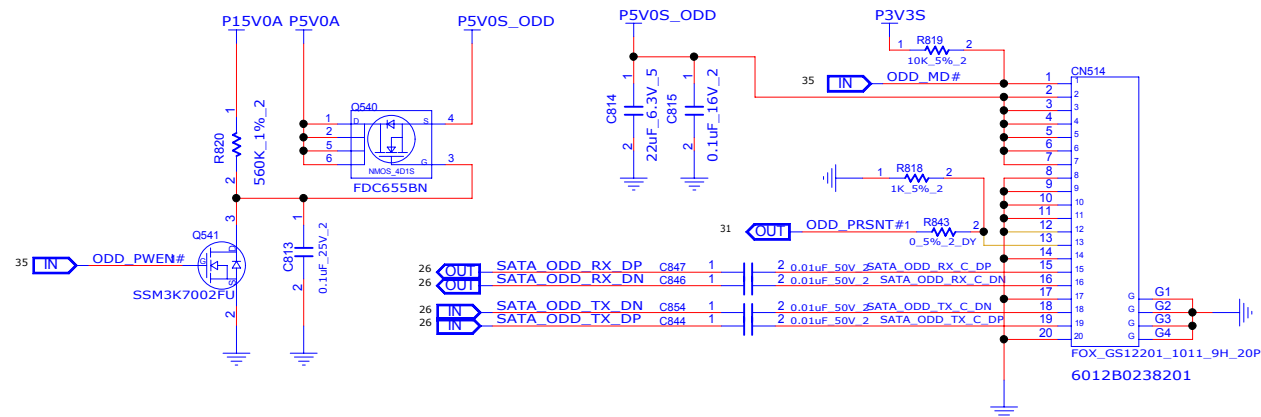
CHANGE by XXX DATE 21-OCT-2002

SHEET 39 of 57

SATA HDD CABLE CONN on MB



SATA ODD CABLE CONN on MB



INVENTEC

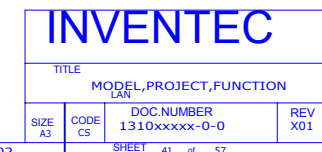
TITLE
MODEL, PROJECT, FUNCTION
SATA HDD & SATA ODD

DOC NUMBER
1310xxxxx-0-0

REV
X01

CHANGE by XXX DATE 21-OCT-2002

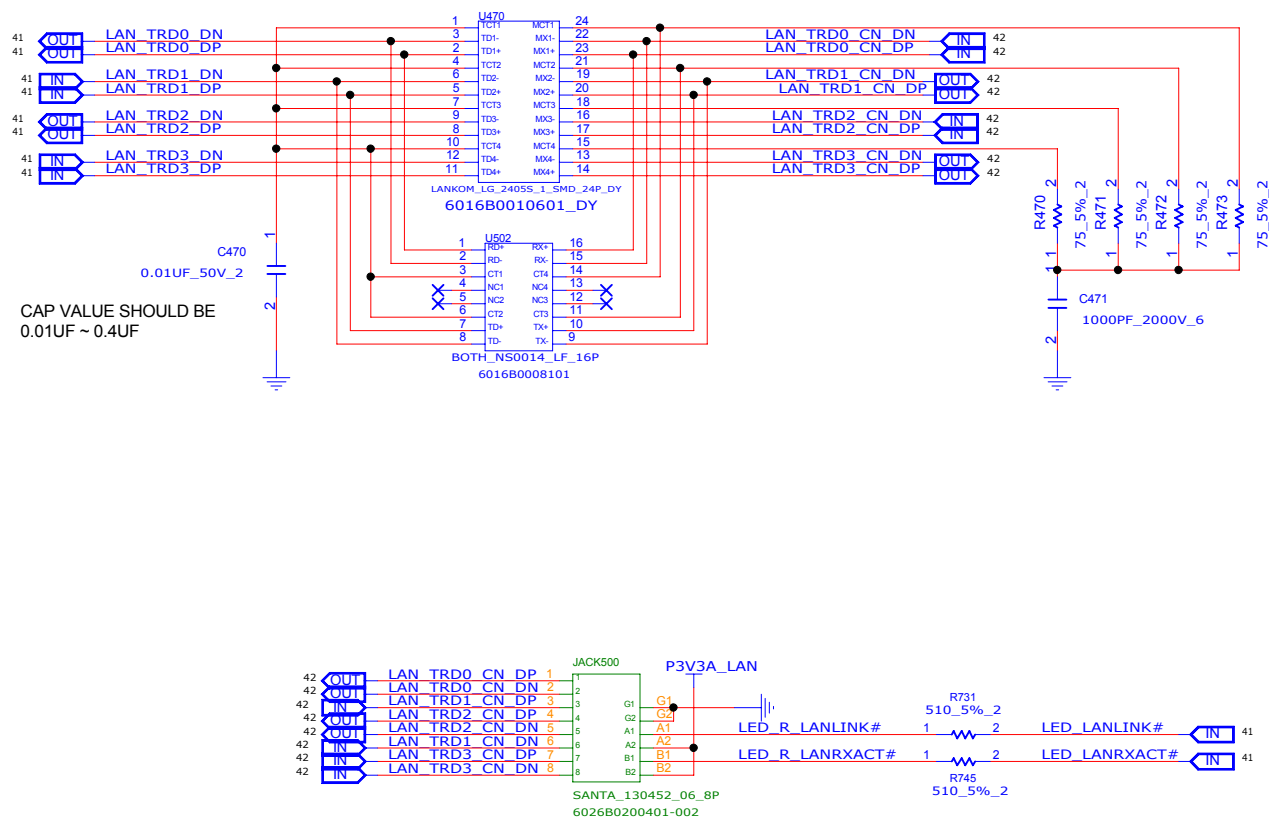
SHEET 40 of 57



TRANSFORMER

U470 FOR GIGALAN

U502 FOR 10/100 LAN



INVENTEC

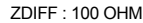
TITLE
MODEL, PROJECT, FUNCTION
RJ45 & TRANSFORMER

SIZE A3 CODE CS DOC NUMBER 1310xxxxx-0-0 REV X01

CHANGE by XXX DATE 21-OCT-2002

SHEET 42 of 57

CARE READER



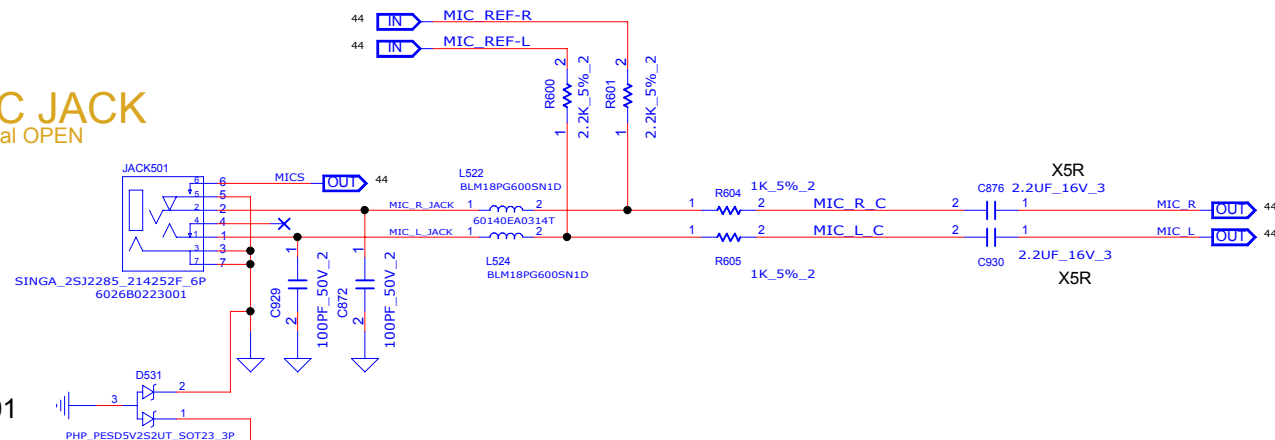
INVENTEC

SIZE A3	CODE CS	DOC.NUMBER 1310xxxxx-0-0	REV X01
SHEET 43 of 57			

JACK CHANGE TO 6026B0223001

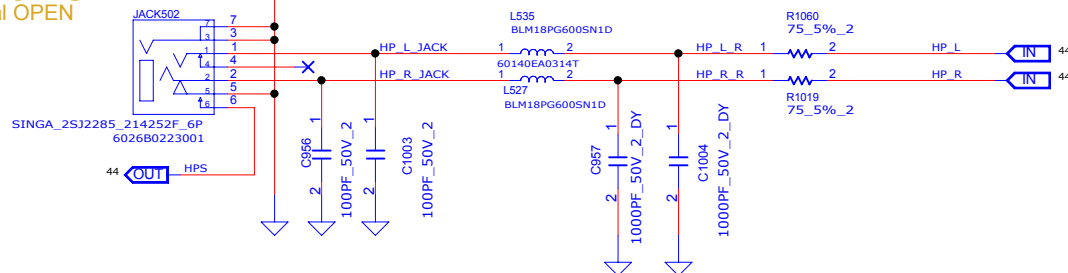
MIC JACK

Normal OPEN



HP JACK

Normal OPEN



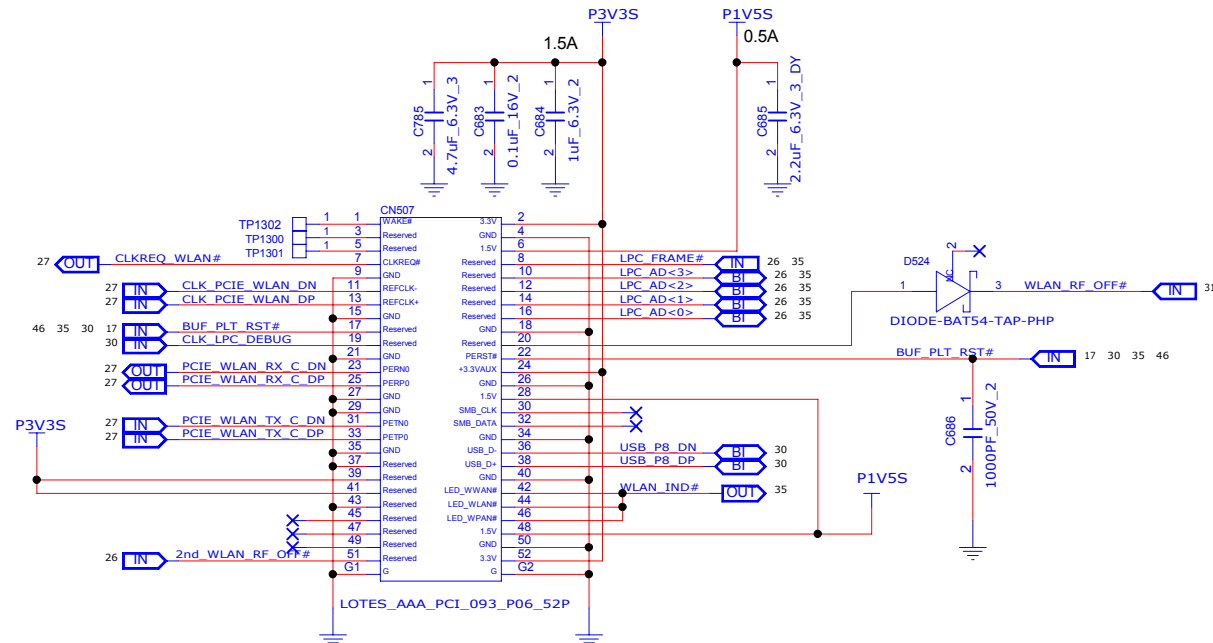
INVENTEC

TITLE			
MODEL,PROJECT,FUNCTION			
HP JACK & MIC JACK			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxxx-0-0	X01

CHANGE by XXX DATE 21-OCT-2002

SHEET 45 of 57

WLAN CONN (MINICARD)



SI build change to 6026B0221502

MARILYN	ATHEROS	AR5B125	VENOR ID : 0X168C	0X1814
ATHEROS	FOXCONN	RALINK RT5390	DEVICE ID : 0X0032	0X539A
AR5B125			(SSID) SUBSYSTEM ID : 0X1838	0X1839
			SUBSYSTEM VENDOR ID : 0X103C	0X103C
			HP P/N : 670036-001	670285-001

INVENTEC

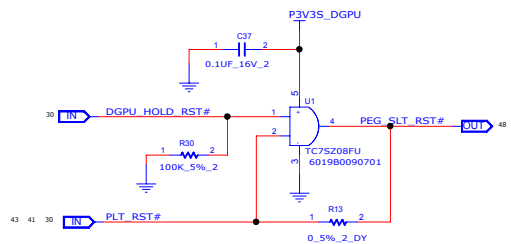
TITLE
MODEL, PROJECT, FUNCTION
WLAN & BT

SIZE A3	CODE CS	DOC NUMBER 1310xxxxx-0-0	REV X01
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SHEET 46 of 57

CHANGE by XXX DATE 21-OCT-2002





THIS PART IS ONLY FOR INTEL PLATFORM

PN : 6019B0818601
PN : 6019B0938301
P/N : APPLY

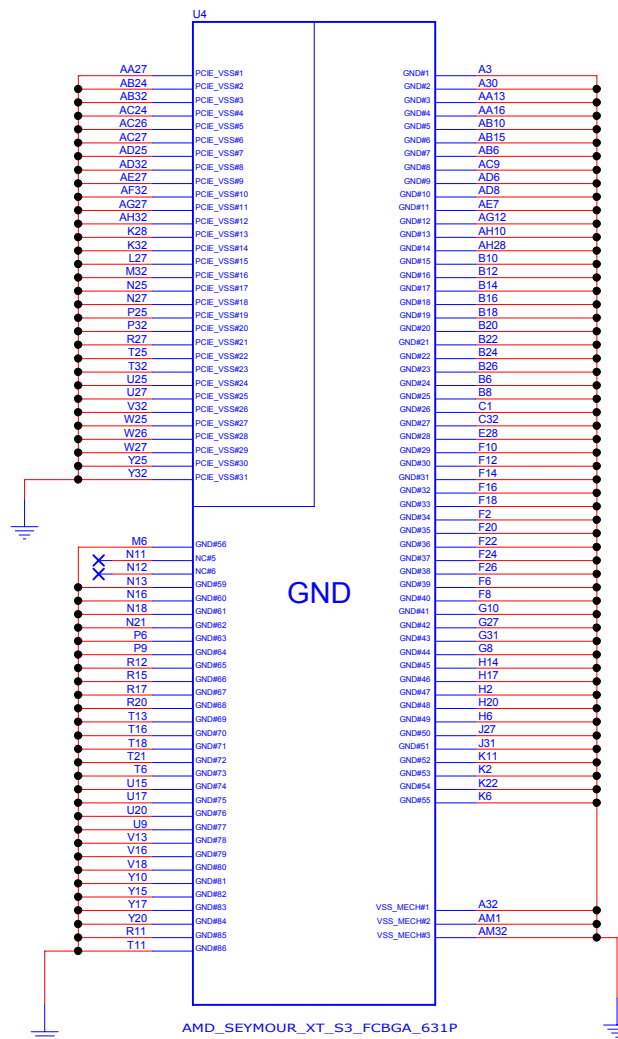
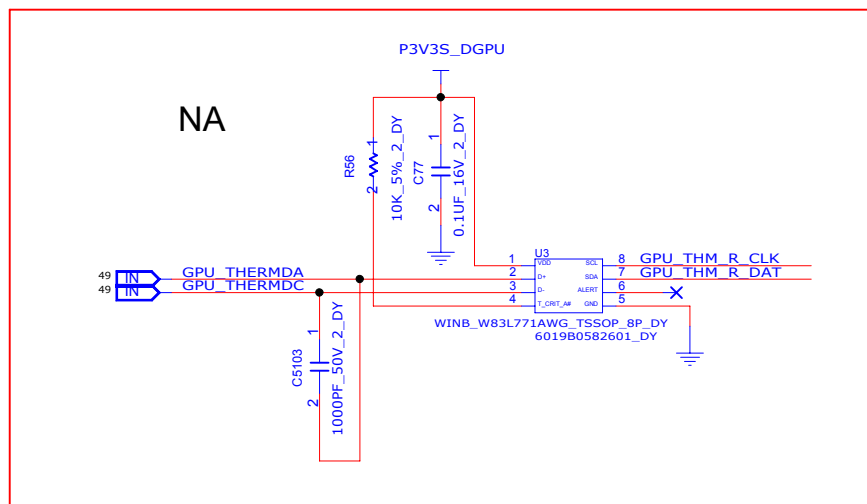
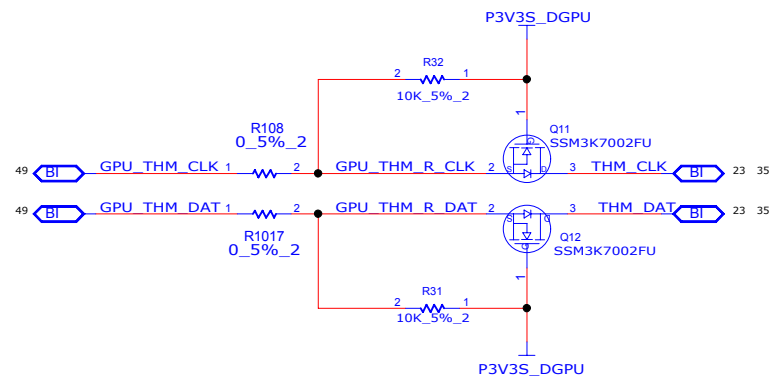
IF GPIO_22_EN=0, THEN GPIO[11:13] DEFINES THE PRIMARY MEMORY APERTURE SIZE			
GPIO_13	GPIO_12	GPIO_11	MEMORY APERTURE SIZE
0	0	1	512/256 MB (DEFAULT)
1	1	0	RESERVED



A horizontal number line with three points marked. From left to right, there is a black dot at the value 1, a red dot at the value 0, and a red dot at the value 0.95V.



TITLE			
MODEL,PROJECT,FUNCTION SEYMOUR XT-S3			
SIZE C	CODE CS	DOC NUMBER 1310xxxxxx-0-0	RI X0
SHEET 49 of 57			



INVENTEC

TITLE			
MODEL PROJECT,FUNCTION			
SEYMOUR_XT-S3			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxxx-0-0	X01

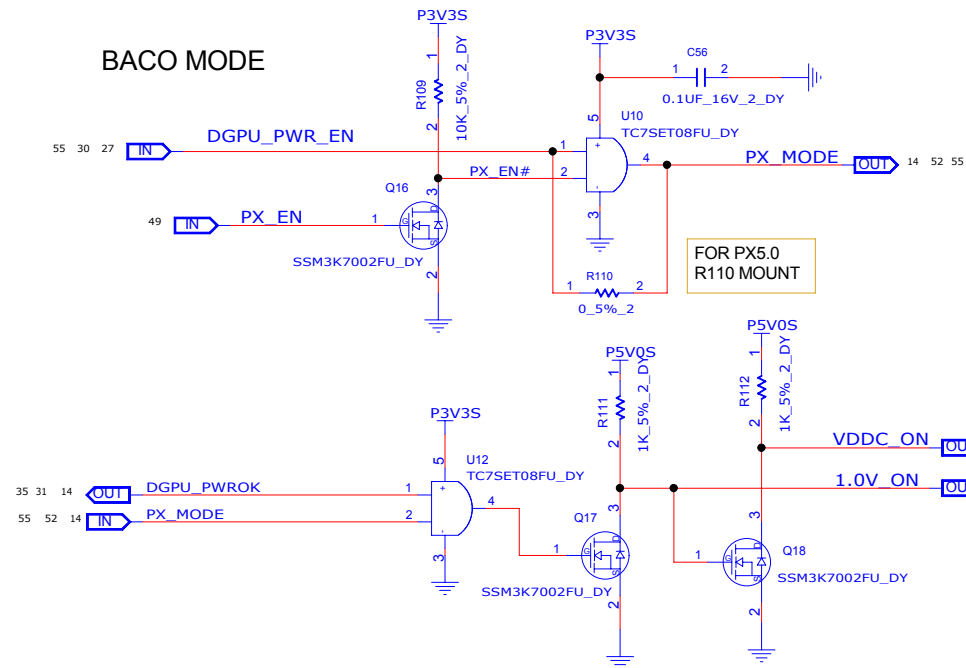
CHANGE by XXX DATE 21-OCT-2002

SHEET 50 of 57

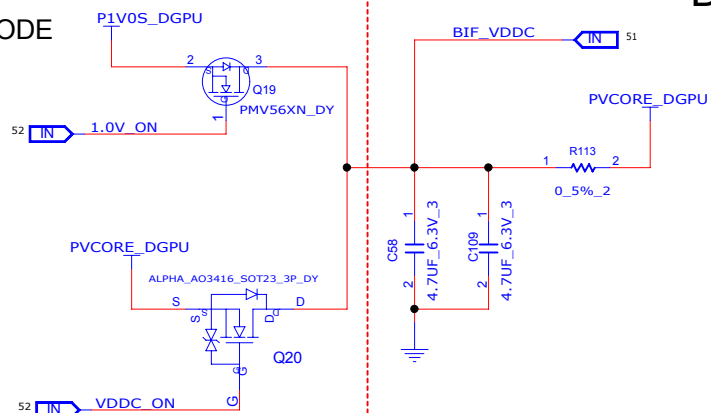


SHEET 51 of 57

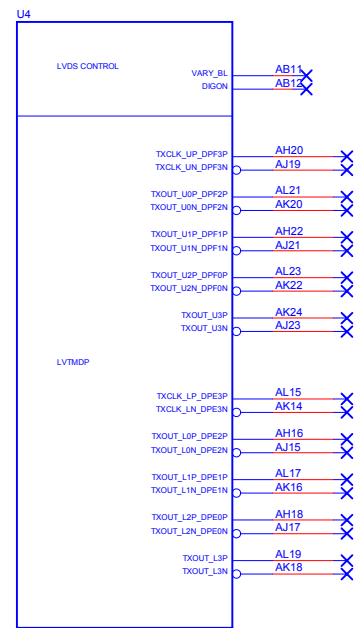
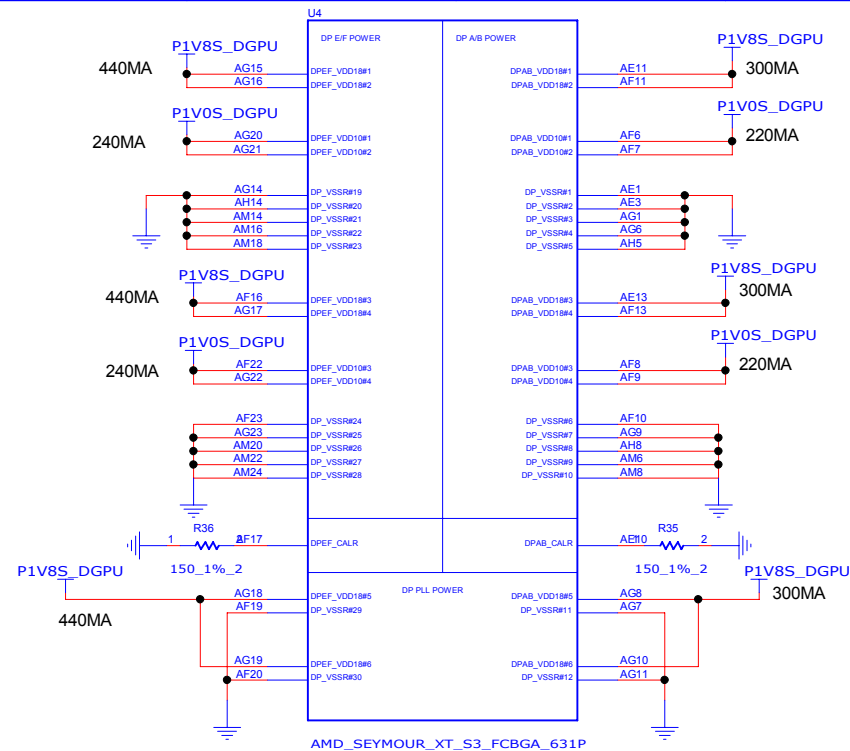
BACO MODE



BACO MODE



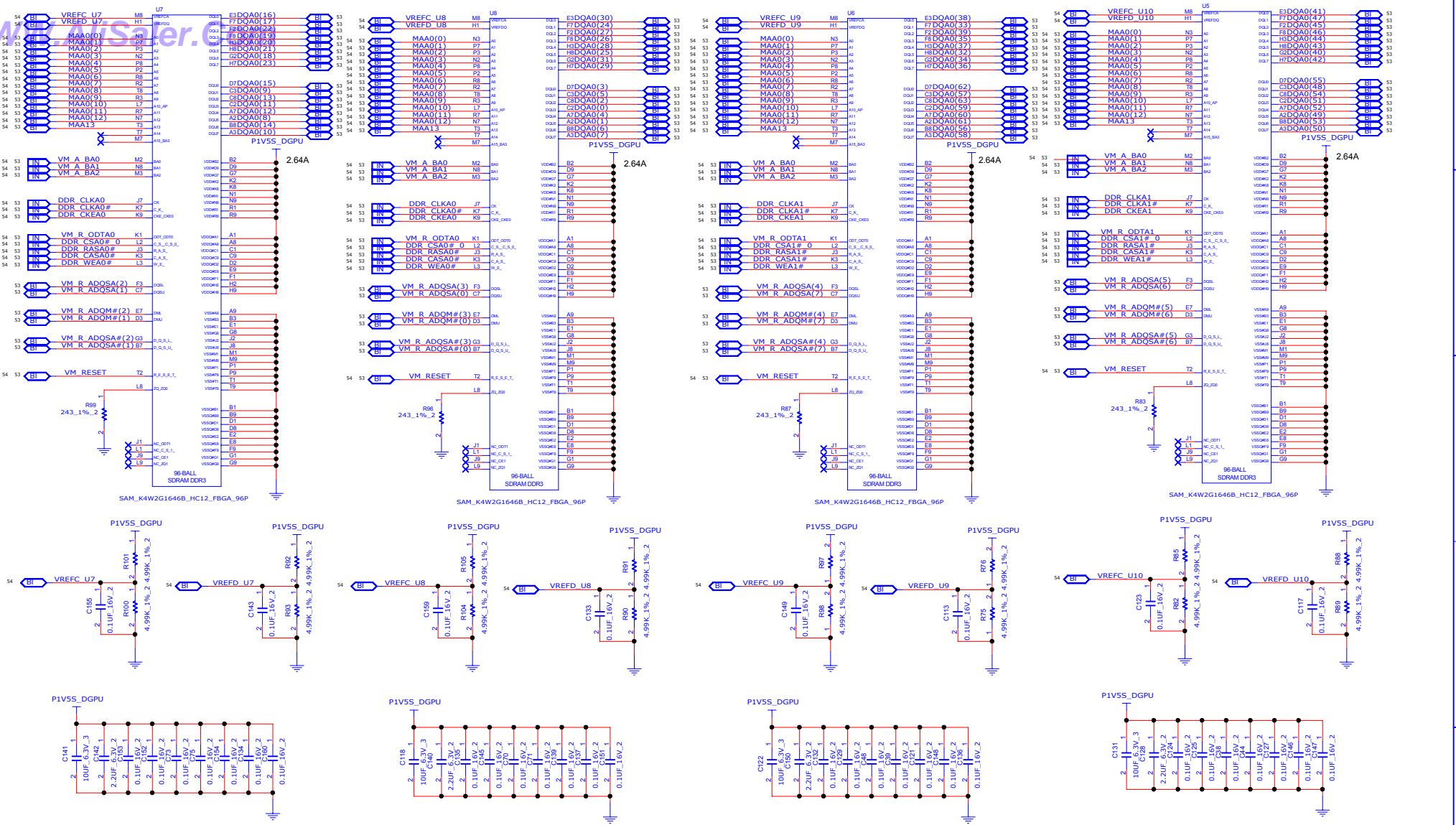
DEFULT IS PX5.0



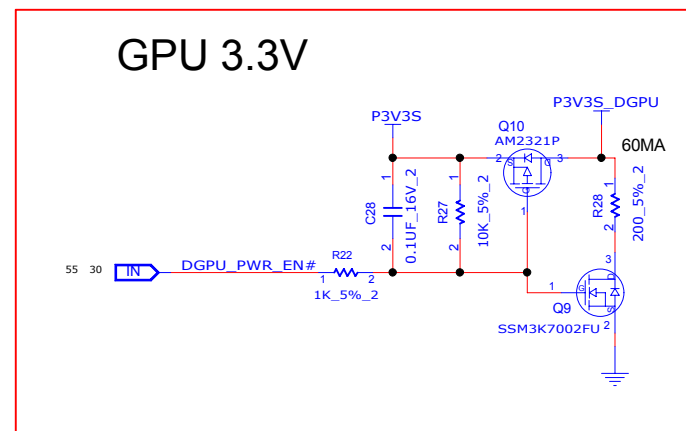
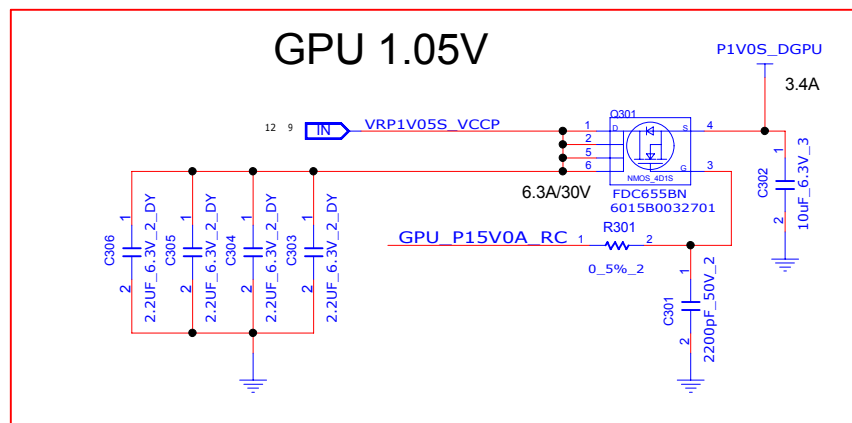
INVENTEC

TITLE			
MODEL PROJECT,FUNCTION			
SEYMOUR XT-S3			
DOC NUMBER			
1310xxxxx-0-0			
REV			
X01			

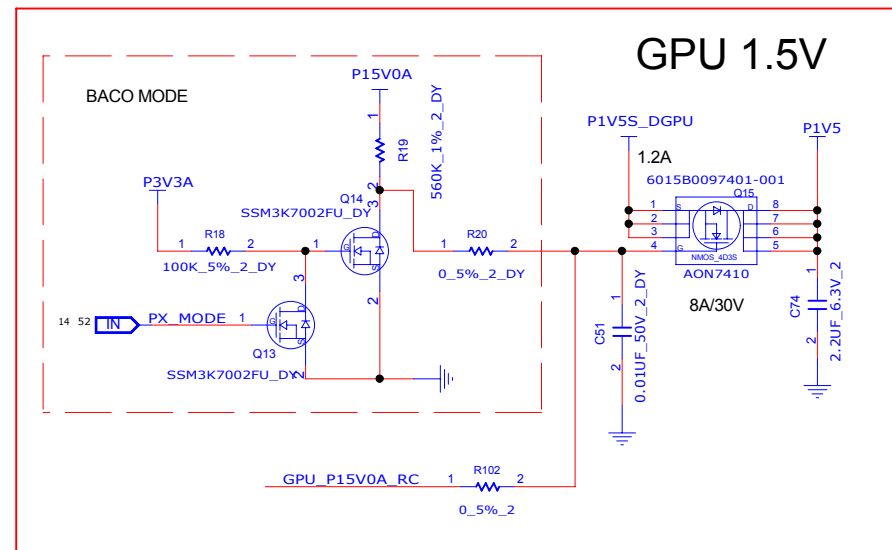
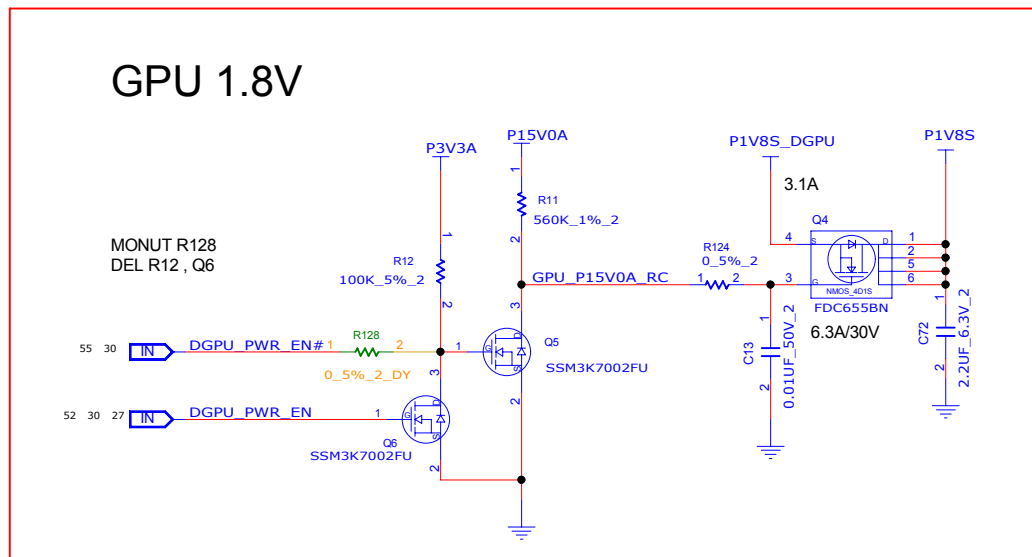




HYNIX 1GB D-DIE PN : 6019B0938301
SAMSUNG 1GB C-DIE PN : 6019B0818601



DEFULT IS PX5.0



INVENTEC

TITLE			
MODEL, PROJECT, FUNCTION			
SIZE A3	CODE CS	DOC NUMBER 1310xxxxx-0-0	REV X01

CHANGE by XXX DATE 21-OCT-2002

SHEET 55 of 57

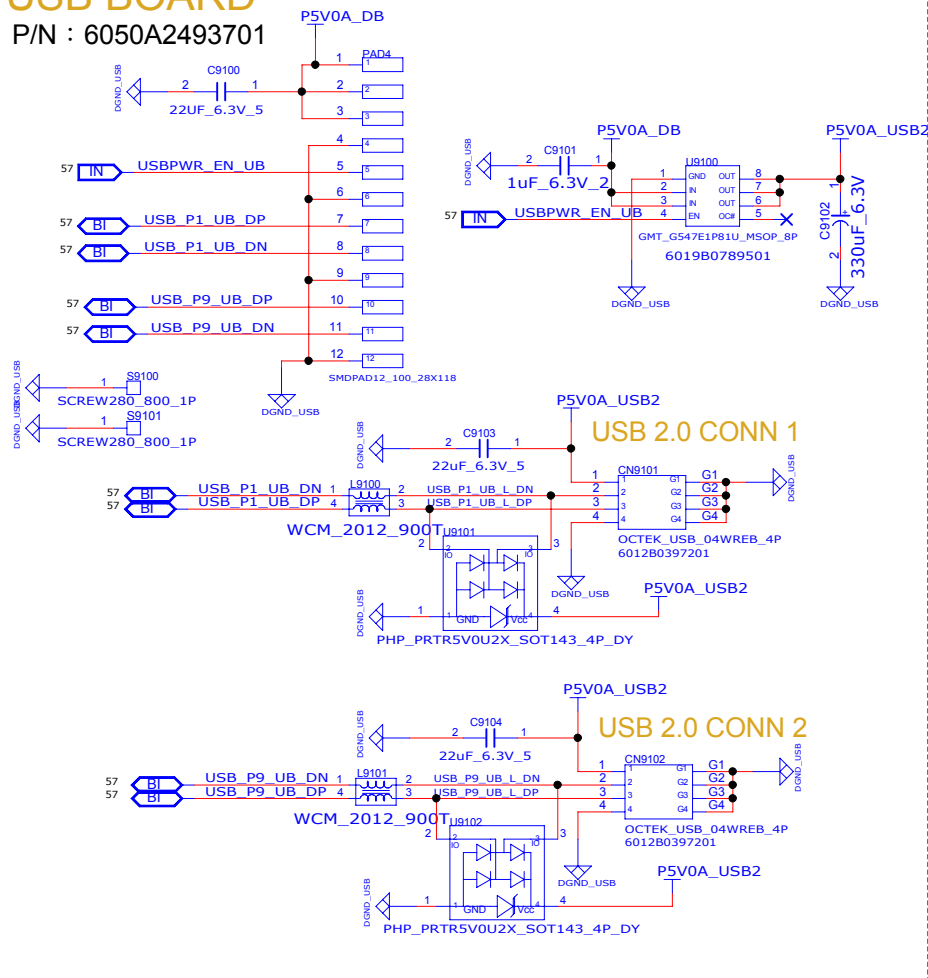


INVENTEC

SHEET 56 of 57

USB BOARD

P/N : 6050A2493701



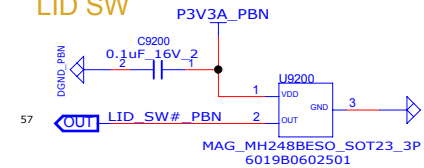
for Dauther Board

FIX_MASK FIX_MASK FIX_MASK FIX_MASK
FIX_MASK FIX_MASK FIX_MASK FIX_MASK
FIX_MASK FIX_MASK FIX_MASK FIX_MASK
FIX_MASK FIX_MASK FIX_MASK FIX_MASK

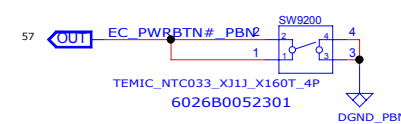
POWER BUTTON BOARD

P/N : 6050A2493201

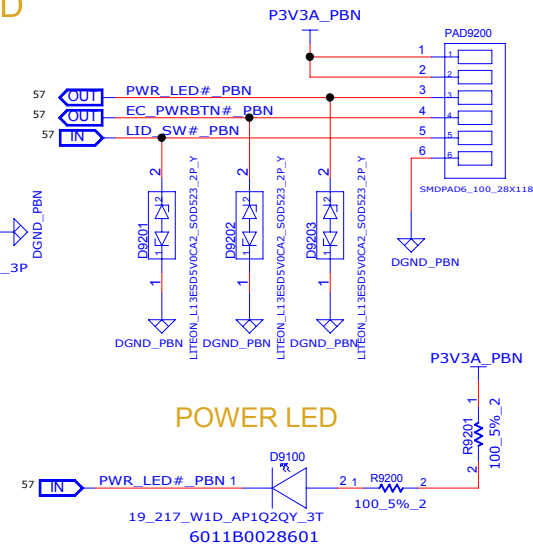
LID SW



POWER BUTTON

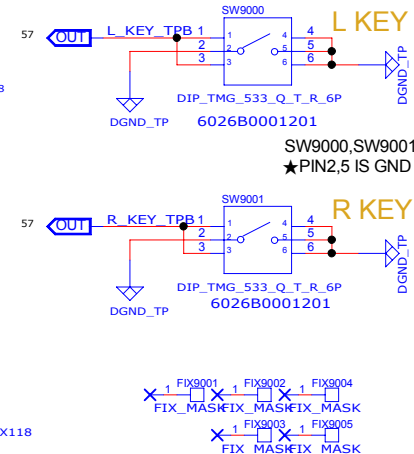
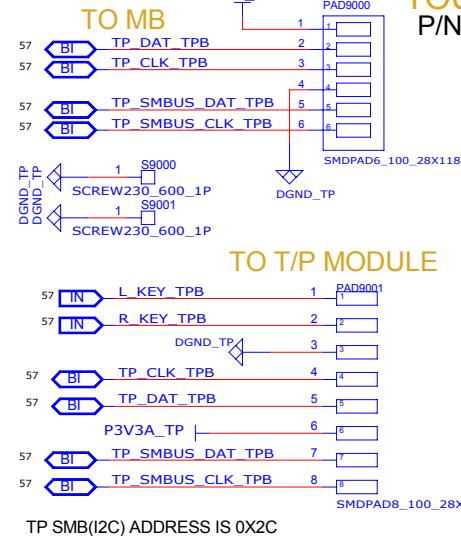


POWER LED



TOUCHPAD R / L BOARD

P/N : 6050A2493601



TP SMB(i2C) ADDRESS IS 0X2C

INVENTEC

TITLE			
MODEL, PROJECT, FUNCTION			
USB, POWER BUTTON DB			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxxx-0-0	X01

CHANGE by XXX DATE 21-OCT-2002

SHEET 57 of 57